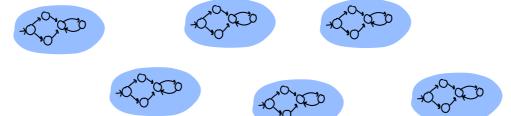
# How to achieve Reachability in Broadcast Networks?

Ongoing work

Journées du GT Verif 21 Novembre 2024, Lille

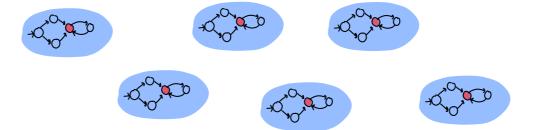
Lucie Guillou IRIF Paris, France Arnaud Sangnier DIBRIS Genova, Italy Tali Sznajder LIP6 Paris, France

#### **Parameterized Broadcast Networks**



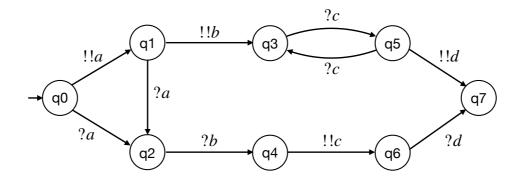
- Unknown number of agents
- Each agent follows a protocol given as a finite-state machine
- Synchronous Communication (Broadcast)
- Interleaving Semantics

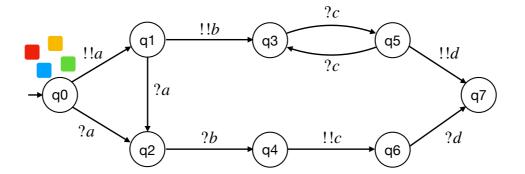
### The Reachability Question

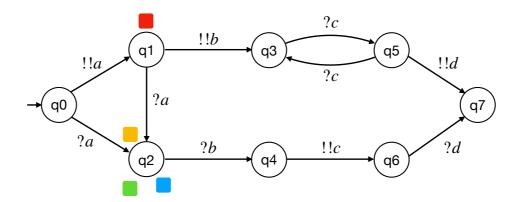


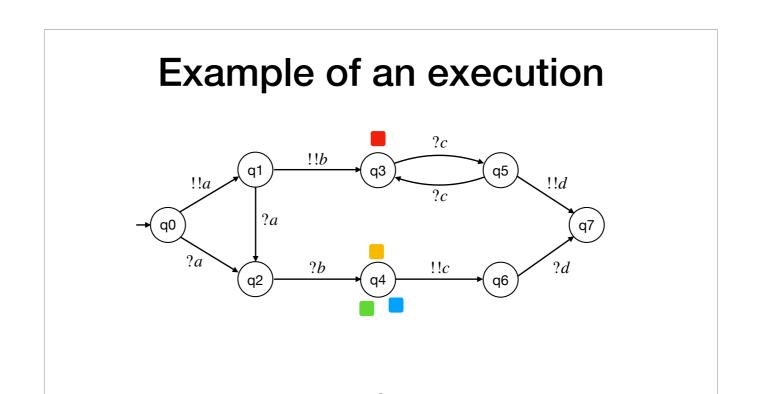
• Is there a number of agents such that there exists a run leading to a bad configuration?

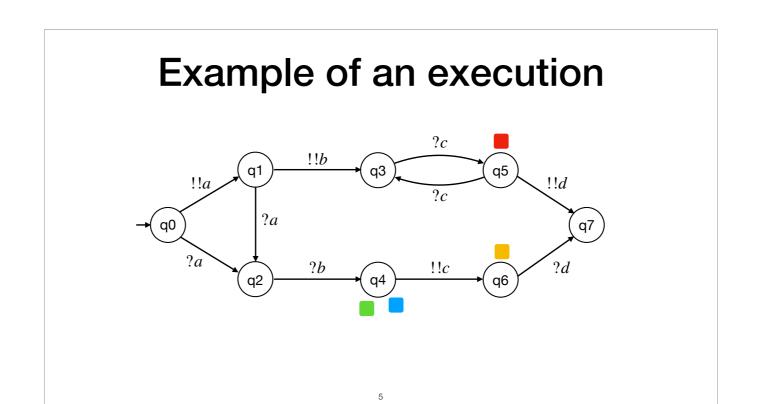
#### **Broadcast Protocols**

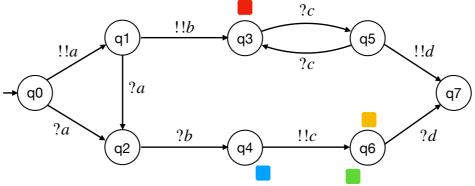


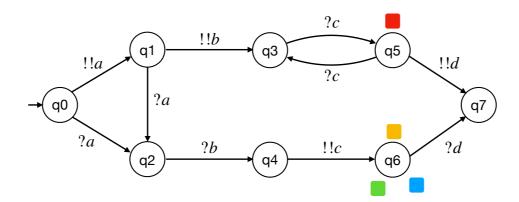


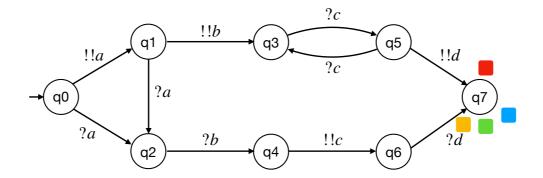




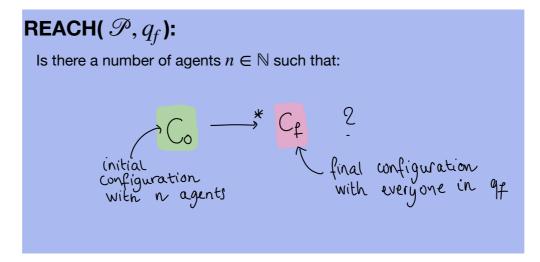




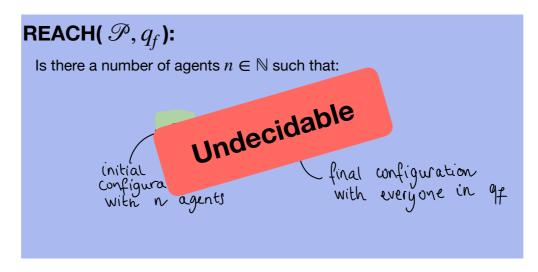




#### The Reachability Problem Formalized

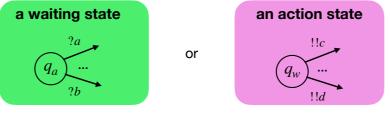


#### The Reachability Problem Formalized



#### A Restriction on Protocols: Wait-Only

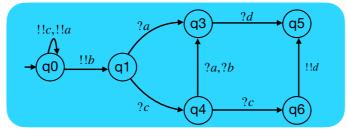
Each state is either:



#### A Restriction on Protocols: Wait-Only

Each state is either:



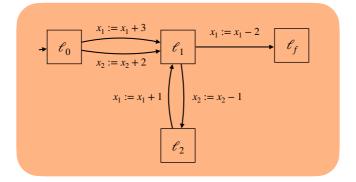


A Wait-Only Protocol

The initial state is always an action state

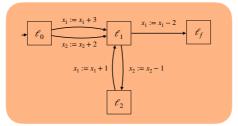
# Vector Addition Systems with States

#### **Vector Addition Systems with States**

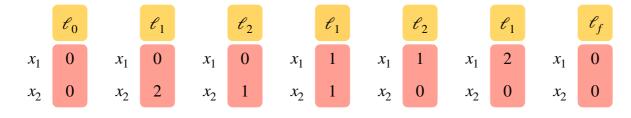


A VASS with two counters  $x_1, x_2$ 

#### **Vector Addition Systems with States**



A VASS with two counters  $x_1, x_2$ 



## Reachability in VASS

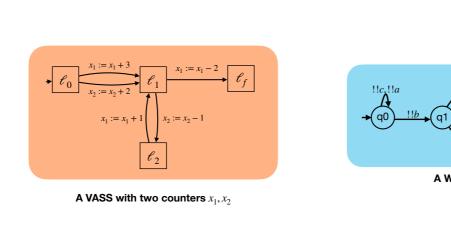
Given a VASS, can we reach  $(\ell_f,0,0)$  from  $(\ell_0,0,0)$ ?

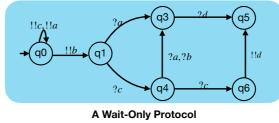
#### Reachability in VASS

Given a VASS, can we reach  $(\ell_f, 0, 0)$  from  $(\ell_0, 0, 0)$ ?

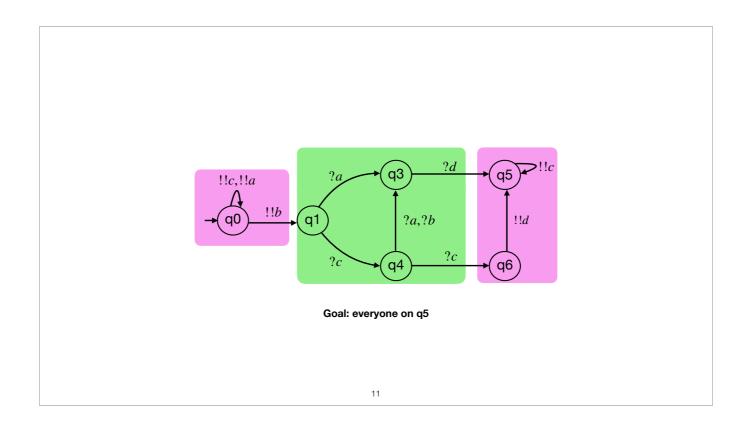
#### **Decidable but Ackermann-hard**

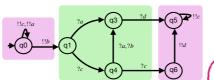
[LerouxSchmitz19] [Leroux'21, CwerwinskiOrlikowski'21]

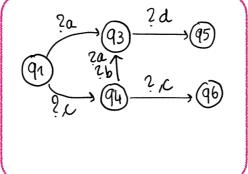




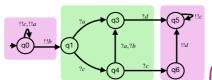
#### Reductions everywhere!

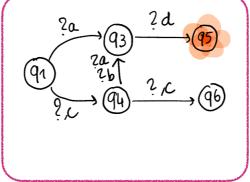




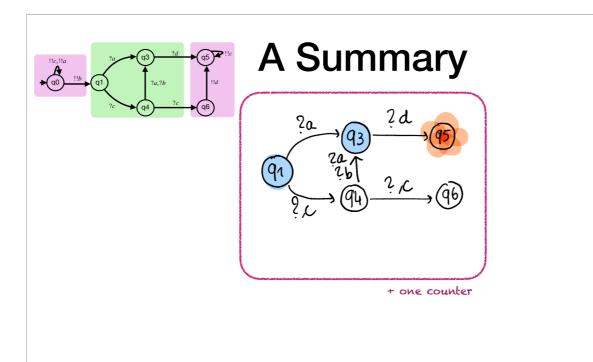


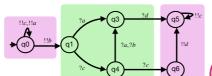
+ one counter

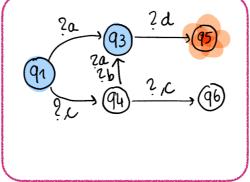




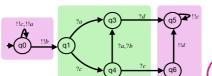
+ one counter

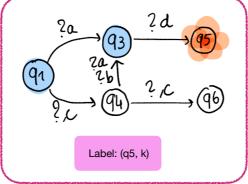




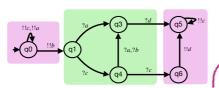


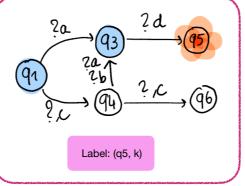
- + one counter
- Some processes are present on q1 and q3,
  the next action state they will reach is q5 and
  they will reach q5 at the same time





- + one counter
- Some processes are present on q1 and q3,
  the next action state they will reach is q5 and
  they will reach q5 at the same time

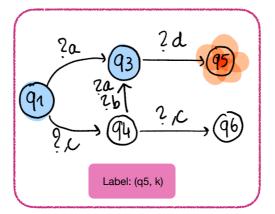


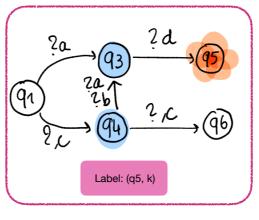


 $1 \le k \le \#(waiting states)$ 

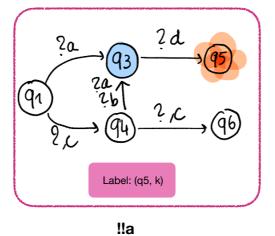
+ one counter

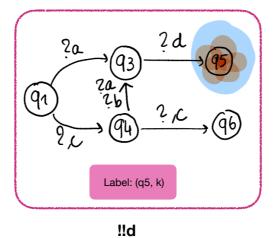
- Some processes are present on 91 and 93,
- the next action state they will reach is qs and they will reach qs at the same time





‼c

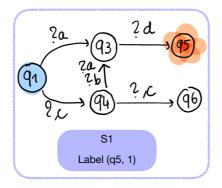


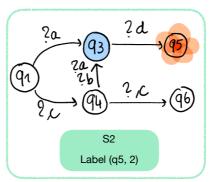


#### **Summaries in VASS**

- Location = coherent set of summaries
- Counters = one counter per action states + one counter per summary label
- In the VASS, we keep track of processes on action states, and guess some summaries for the processes on waiting states

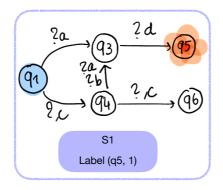
#### Coherent\* sets of Summaries

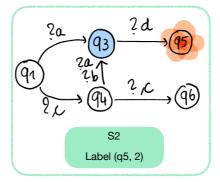




\* two processes on different summaries don't reach the same state OR reach the same state but not at the same time

#### Coherent\* sets of Summaries

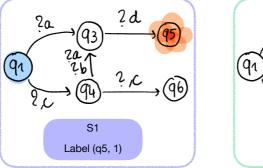


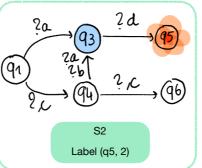


ex: !!d !!a !!d

\* two processes on different summaries don't reach the same state OR reach the same state but not at the same time

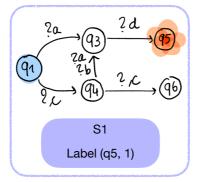
#### **Coherent\* sets of Summaries**

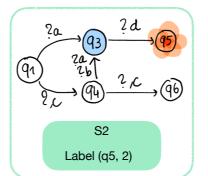


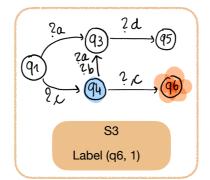


\* two processes on diff Coherence
OR reach the same state but not at the same time

### **Coherent\* sets of Summaries**

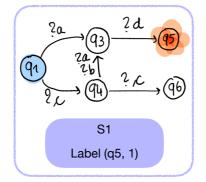


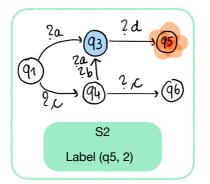


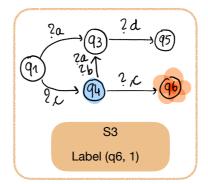


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### **Coherent\* sets of Summaries**



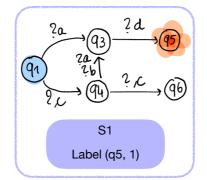


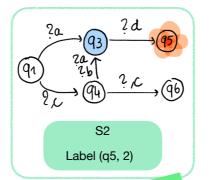


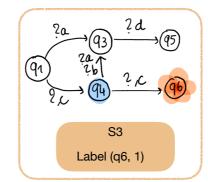
ex: !!d !!c !!b !!d

<sup>\*</sup> two processes on different summaries don't reach the same state OR reach the same state but not at the same time

#### Coherent\* sets of Summaries



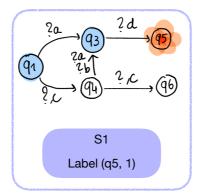


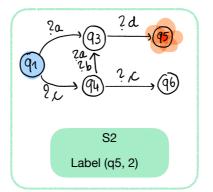


coherent again!

<sup>\*</sup> two processes on different summaries don't reach the same state OR reach the same state but not at the same time

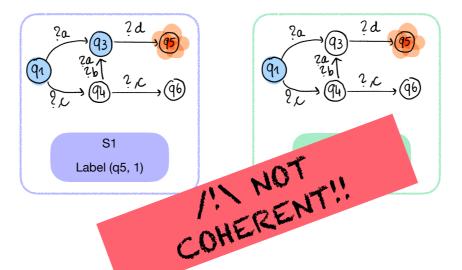
### **Coherent sets of Summaries**





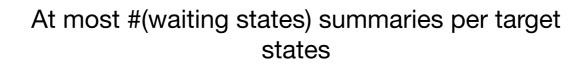
\* two processes on different summaries don't reach the same state OR reach the same state but not at the same time

#### **Coherent sets of Summaries**

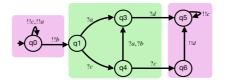


\* two processes on different summa

on't reach the same state OR reach the same state but not at the same time

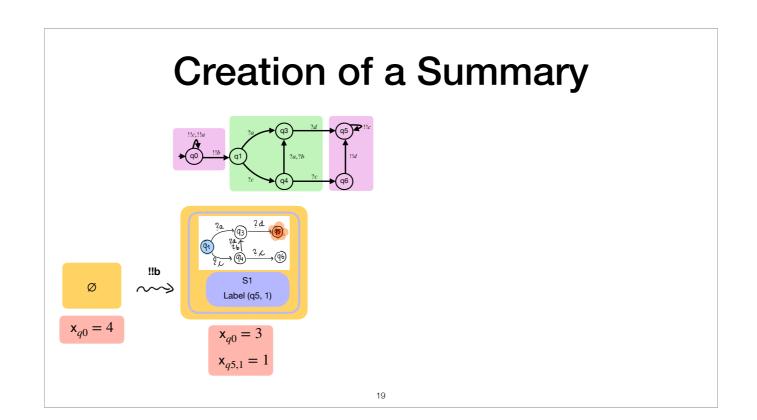


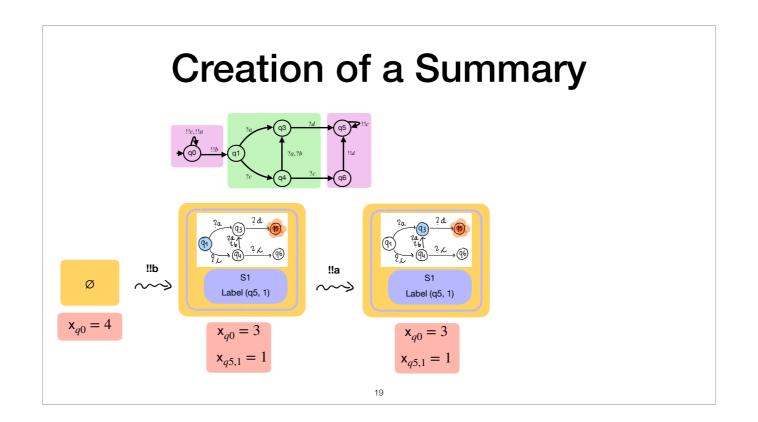
## Creation of a Summary

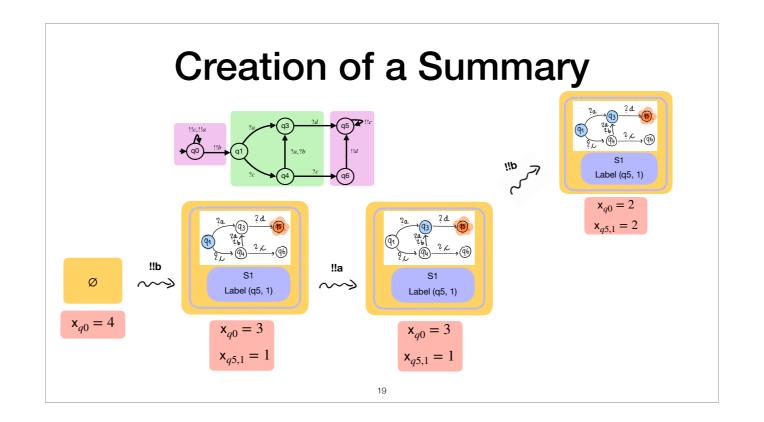


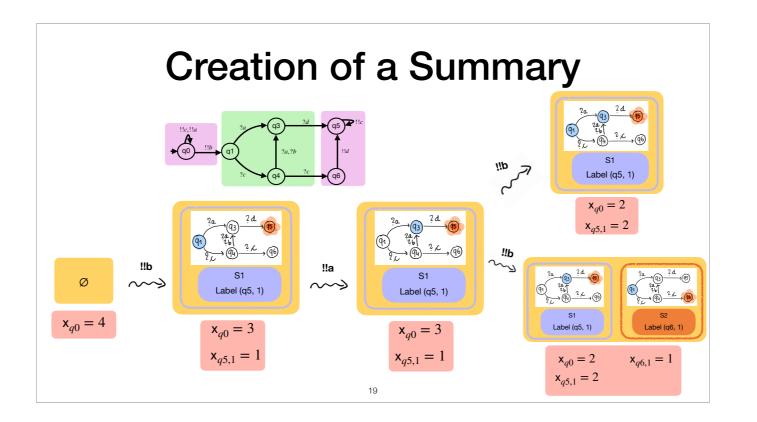
Ø

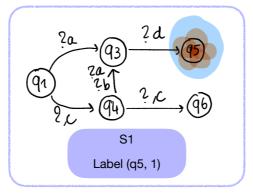
$$x_{a0} = 4$$



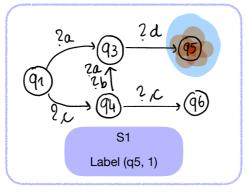






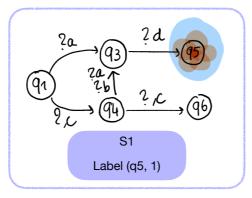


$$\mathsf{x}_{q5,1} = 4$$



$$\mathsf{x}_{q5,1} = 4$$

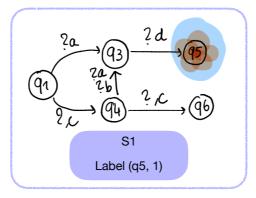
Everyone has arrived on 95, what should we do?



 $\mathsf{x}_{q5,1} = 4$ 

Everyone has arrived on 95, what should we do?

Forget about the summary

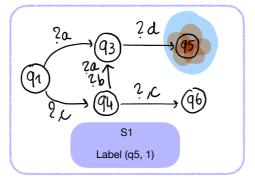


$$x_{q5,1} = 4$$

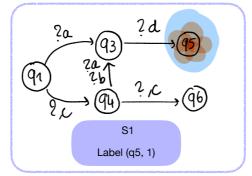
Everyone has arrived on 95, what should we do?

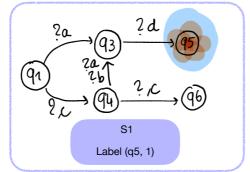
Forget about the summary

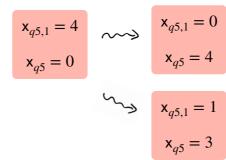
Transfer the counter  $\mathbf{x}_{q5,1}$  to  $\mathbf{x}_{q5}$ 

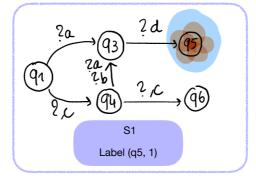


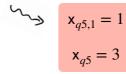
$$\mathbf{x}_{q5,1} = 4$$
$$\mathbf{x}_{q5} = 0$$











#### Not a problem!

We let a process asleep on q5 until we re use label (q5,1) and re transfer the counter

#### Conclusion

- Reachability for Wait-Only protocols is decidable but Ackermann-hard
- Model Checking W-O protocols against LTL specification is EXPSPACE-complete (cf. [Habermehl'97])
- Single-Wait-Only protocols (update: two cases, one easy to solve, one hard to solve (??))

