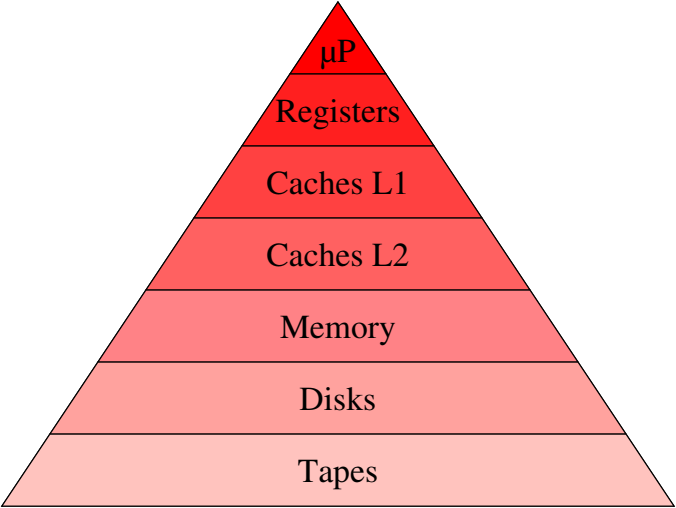




μ P

Registers

Caches L1

Caches L2

Memory

Disks

Tapes