

Concurrent Separation Logic Meets Template Games

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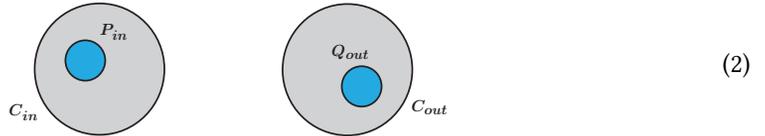
An old dream of concurrency theory and programming language semantics has been to uncover the fundamental synchronization mechanisms which regulate situations as different as game semantics for higher-order programs, and Hoare logic for concurrent programs with shared memory and locks. In this paper, we establish a deep and unexpected connection between two recent lines of work on concurrent separation logic (CSL) and on template game semantics for differential linear logic (DiLL). Thanks to this connection, we reformulate in the purely conceptual style of template games for DiLL the asynchronous and interactive interpretation of CSL designed by Melliès and Stefanesco. We believe that the analysis reveals something important about the secret anatomy of CSL, and more specifically about the subtle interplay, of a categorical nature, between sequential composition, parallel product, errors and locks.

1 INTRODUCTION

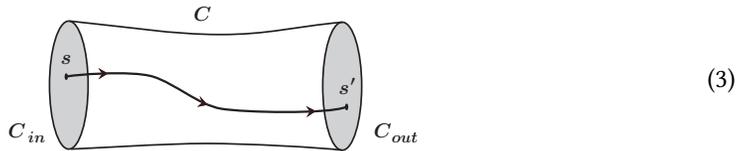
There is a fascinating analogy between the notion of Hoare triple $\{P\}C\{Q\}$ in programming language semantics, and a stream of elegant ideas coming from differential geometry and mathematical physics. Consider the typical situation of a code C written in an imperative and sequential programming language, and executed on a set $State$ of machine states. By construction, the code C comes equipped with a set C_{in} of input states and a set C_{out} of output states, together with a pair of labeling functions

$$\lambda_{in} : C_{in} \rightarrow State \quad \lambda_{out} : C_{out} \rightarrow State \quad (1)$$

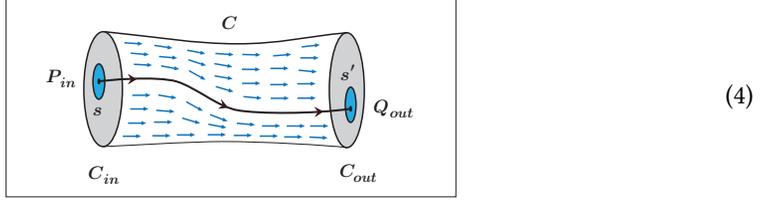
which assign to every input state $s \in C_{in}$ and output state $s' \in C_{out}$ of the code C its underlying machine state $\lambda_{in}(s) \in State$ and $\lambda_{out}(s') \in State$. Following the philosophy of Hoare logic, the predicates P and Q describe specific subsets of the set $State$ of states of the machine, which induce (by inverse image) predicates P_{in} on C_{in} and Q_{out} on C_{out} . The Hoare triple $\{P\}C\{Q\}$ then expresses the fact that the code C transports every input state $s \in C_{in}$ which satisfies the predicate P_{in} into an output state $s' \in C_{out}$ which satisfies the predicate Q_{out} . Looking at the situation with the eyes of the physicist, the set C_{in} of input states can be depicted as a two-dimensional disk (in gray) with the predicate P_{in} represented as a subset or subdisk (in blue) living inside C_{in} ; and similarly for the predicate Q_{out} on the set C_{out} of output states:



Since the purpose of the code C is to transport the input states $s \in C_{in}$ to output states $s' \in C_{out}$, it makes sense to depict C (in white) as a three-dimensional tube or cylinder connecting the input disk C_{in} to the output disk C_{out} . Here, the three-dimensional cylinder C should be understood as a geometric object living in space and time, and describing the evolution in time of the internal states of the code C in the course of execution:



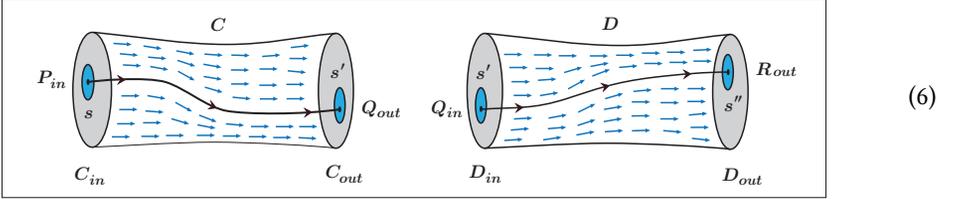
The physical intuition that the code C transports states $s \in C_{in}$ to states $s' \in C_{out}$ may be expressed by equipping the cylinder with a (three-dimensional) vector field describing how a state evolves in time through the code C . The description is reminiscent of fluid mechanics, and the way one describes the trajectory of a particle along a flow in the cylinder C . In that graphical representation, the Hoare triple $P\{C\}Q$ indicates that the flow of execution described by the vector field on the cylinder C transports every state $s \in C_{in}$ starting from P_{in} to a state $s' \in C_{out}$ exiting in Q_{out} :



This intuition underlies the sequential rule of Hoare logic

$$\frac{\{P\}C\{Q\} \quad \{Q\}D\{R\}}{\{P\}C; D\{R\}} \quad \text{sequential composition} \quad (4)$$

which states that Hoare triples “compose well” in the sense that the Hoare triple $\{P\}C; D\{R\}$ holds whenever the Hoare triples $\{P\}C\{Q\}$ and $\{Q\}D\{R\}$ are supposed to hold. This basic principle of Hoare logic reflects the fact that when the two codes C and D are executed sequentially as below



every state $s \in C_{in}$ satisfying the predicate P_{in} is transported by the code C to a transitory state $s' \in C_{out}$ satisfying the predicate Q_{out} ; and that the same transitory state $s' \in D_{in}$ taken now as input is transported by the code D to a state $s'' \in D_{out}$ satisfying the predicate R_{out} . The careful reader will notice that we make here the simplifying and somewhat unrealistic assumption that the set C_{out} of output states of the code C coincides with the set D_{in} of input states of the code D ; as we will see, this point is interesting and far from anecdotal, and we will thus come back to it with great attention at a later stage of the paper, see §5.

Cospans of transition systems. By taking seriously these geometric intuitions and formalizing them in the language of category theory, we establish in this paper a strong and unexpected connection between two recent and largely independent lines of work on concurrent separation logic [18, 19] and on template games for differential linear logic [16, 17]. The connection enables us to disclose for the first time a number of basic and fundamental categorical structures underlying concurrent separation logic, and more specifically, the proof of the asynchronous soundness theorem established in [19]. Our starting point is to define a **transition system** (A, λ_A) on a given labeling graph label as a morphism

$$\lambda_A : A \longrightarrow \text{label} \quad (7)$$

in the category **Gph** of directed graphs. The graph A is called the *support* of the transition system (A, λ_A) , and λ_A its *labeling map*. It is important here that the transition system (A, λ_A) is labeled with a graph instead of a set, and that it is multi-sorted with sorts provided by the vertices of the

labeling graph \mathfrak{s}_{label} . A morphism between two such transition systems

$$(f, \varphi) : (A, \lambda_A) \longrightarrow (B, \lambda_B) \quad (8)$$

is defined as a pair (f, φ) of graph morphisms making the diagram below commute

$$\begin{array}{ccc} A & \xrightarrow{f} & B \\ \lambda_A \downarrow & & \downarrow \lambda_B \\ \mathfrak{s}_{label,1} & \xrightarrow{\varphi} & \mathfrak{s}_{label,2} \end{array} \quad (9)$$

in the category **Gph**. The graph morphism $\varphi : \mathfrak{s}_{label,1} \rightarrow \mathfrak{s}_{label,2}$ between labeling graphs works in the same way as a “change of base” and is called the *relabeling map* of the morphism (8). At this stage, we are ready to formulate the guiding idea of the paper, which is that the situation of the three-dimensional cylinder C connecting the sets C_{in} and C_{out} of input and output states depicted in (3) can be conveniently formulated as a *cospan of transition systems*

$$(C_{in}, \lambda_{in}) \xrightarrow{(in, \eta)} (C, \lambda_{code}) \xleftarrow{(out, \eta)} (C_{out}, \lambda_{out}) \quad (10)$$

defining a commutative diagram in the category **Gph**:

$$\begin{array}{ccccc} C_{in} & \xrightarrow{in} & C & \xleftarrow{out} & C_{out} \\ \lambda_{in} \downarrow & & \downarrow \lambda_{code} & & \downarrow \lambda_{out} \\ \mathfrak{s}_S[0] & \xrightarrow{\eta} & \mathfrak{s}_S[1] & \xleftarrow{\eta} & \mathfrak{s}_S[0] \end{array} \quad (11)$$

One important feature of the cospan formulation of (3) is the simple and intuitive definition of the labeling graphs $\mathfrak{s}_S[0]$ and $\mathfrak{s}_S[1]$ and of the relabeling map $\eta : \mathfrak{s}_S[0] \rightarrow \mathfrak{s}_S[1]$. Both labeling graphs $\mathfrak{s}_S[0]$ and $\mathfrak{s}_S[1]$ have the set *State* of machine states as set of vertices. The difference between them is that the graph $\mathfrak{s}_S[0]$ is discrete (that is, it has no edge) and can be thus identified with the set *State* itself, while the graph $\mathfrak{s}_S[1]$ has as edges the transitions $inst : s \rightarrow s'$ performed by the instructions $inst \in Inst$ of the machine. The relabeling map

$$\eta : \mathfrak{s}_S[0] \longrightarrow \mathfrak{s}_S[1] \quad (12)$$

is the graph morphism which maps every machine state $s \in State$ to itself. The graph C together with the label map $\lambda_{code} : C \rightarrow \mathfrak{s}_S[1]$ describe the transition system (C, λ_{code}) defined by the operational semantics of the code. Note in particular that λ_{code} labels every vertex (or internal state) of the graph C with a machine state $\lambda_{code}(s) \in State$ and every edge (or execution step) $m : s \rightarrow s'$ of the graph C with a machine instruction $\lambda_{code}(m) : \lambda_{code}(s) \rightarrow \lambda_{code}(s')$. The sets C_{in} and C_{out} of input and output states of the code C are understood in (10) as discrete graphs, and similarly for the functions $\lambda_{in} : C_{in} \rightarrow State$ and $\lambda_{out} : C_{out} \rightarrow State$ in (1) which are understood as graph homomorphisms. Similarly, the source and target maps $in : C_{in} \rightarrow C$ and $out : C_{out} \rightarrow C$ are graph morphisms whose purpose is to map every input $s \in C_{in}$ and output state $s' \in C_{out}$ to the underlying internal states $in(s)$ and $out(s')$ of the code C .

The bicategory of cospans. The discussion leads us to the definition of the bicategory $\mathbf{Cospan}(\mathbb{S})$ of cospans associated to a category \mathbb{S} with pushouts. The bicategory $\mathbf{Cospan}(\mathbb{S})$ has the same objects A, B, C as the original category \mathbb{S} , and its morphisms are the triples

$$(S, in, out) : A \multimap B \quad (13)$$

consisting of an object S of the category \mathbb{S} together with a pair

$$A \xrightarrow{\text{in}} S \xleftarrow{\text{out}} B$$

of morphisms of the category \mathbb{S} . Such a triple (13) is called a **cospan** between A and B , with **support** the object S of the category \mathbb{S} . In many situations, the two morphisms $\text{in} : A \rightarrow S$ and $\text{out} : B \rightarrow S$ can be deduced from the context, and we thus simply write $S : A \dashrightarrow B$ for the cospan in that case. A 2-dimensional cell in the bicategory $\mathbf{Cospan}(\mathbb{S})$ between two such cospans

$$\varphi : (S, \text{in}, \text{out}) \Longrightarrow (T, \text{in}, \text{out}) : A \dashrightarrow B$$

is defined as a morphism $\varphi : S \rightarrow T$ of the original category \mathbb{S} , making the diagram commute:

$$\begin{array}{ccccc} & & S & & \\ & \text{in} \nearrow & & \nwarrow \text{out} & \\ A & & & & B \\ & \nwarrow \text{in} & & \nearrow \text{out} & \\ & & T & & \end{array}$$

Two cospans $S : A \dashrightarrow B$ and $T : B \dashrightarrow C$ can be put side by side and composed into the cospan $T \circ S : A \dashrightarrow C$ by “gluing” their common border B , using a *pushout diagram* performed in the category \mathbb{S} :

$$\begin{array}{ccccc} & & B & & \\ & \text{out} \nearrow & & \nwarrow \text{in} & \\ A & \xrightarrow{\text{in}} & S & & T \\ & \searrow \text{inl} & & \nearrow \text{inr} & \\ & & T \circ S & & C \end{array}$$

Sequential composition as pushout + relabeling. One good reason for describing codes C and D as cospans (10) in the category \mathbf{Trans} of transition systems, is that the very same “gluing” recipe based on pushouts can be applied to compute their *sequential composition* $C;D$, at least in the specific case where the set C_{out} of output states of C coincides with the set D_{in} of input states of D . The pushout construction performed in \mathbf{Trans} gives rise to the commutative diagram in \mathbf{Gph} below:

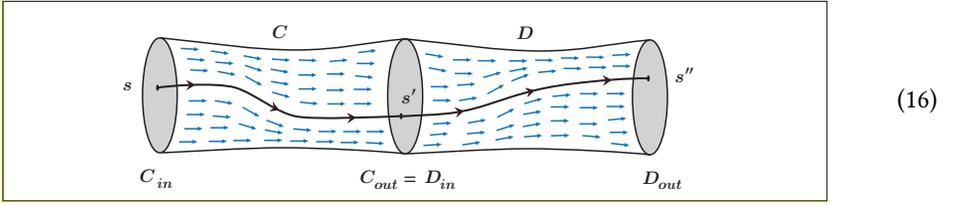
$$\begin{array}{ccccc} C_{\text{in}} & & C_{\text{out}} = D_{\text{in}} & & D_{\text{out}} \\ \lambda_{\text{in}} \downarrow & \text{in} \nearrow & \downarrow \lambda_{\text{out}/\text{in}} & \nwarrow \text{in} & \downarrow \lambda_{\text{out}} \\ \mathfrak{s}_S[0] & & C & & D & & \mathfrak{s}_S[0] \\ & \searrow \eta & \downarrow \lambda_C & \nearrow \eta & \downarrow \lambda_D & \nwarrow \eta & \\ & & \mathfrak{s}_S[1] & & C;D & & \mathfrak{s}_S[1] \\ & \text{inl} \nearrow & \downarrow \lambda_{\text{po}} & \nwarrow \text{inr} & & & \\ & & \mathfrak{s}_S[2] & & & & \end{array} \quad (14)$$

It should be noted that the pushout diagram in \mathbf{Trans} is obtained by computing *independently* in \mathbf{Gph} the pushout diagram [a] defining the support graph $C;D$ as well as the pushout diagram [b]

defining the labeling graph $\mathfrak{s}_S[2]$, as shown below:

$$\begin{array}{ccc}
 \begin{array}{ccc}
 & C_{in} = D_{out} & \\
 \swarrow \text{out} & & \searrow \text{in} \\
 C & & D \\
 \searrow \text{inl} & \text{pushout}[a] & \swarrow \text{inr} \\
 & C; D &
 \end{array}
 &
 &
 \begin{array}{ccc}
 & \mathfrak{s}_S[0] & \\
 \swarrow \eta & & \searrow \eta \\
 \mathfrak{s}_S[1] & & \mathfrak{s}_S[1] \\
 \searrow \text{inl} & \text{pushout}[b] & \swarrow \text{inr} \\
 & \mathfrak{s}_S[2] &
 \end{array}
 \end{array} \quad (15)$$

Pictorially, the purpose of the pushout $[a]$ is to “glue” together the two cylinders C and D represented in (6) along their common border $C_{out} = D_{in}$, so as to obtain the cylinder $C; D$ describing the result of composing the two codes C and D sequentially:



One main contribution and novelty of the paper is the idea that the pushout diagram $[a]$ performed on the graphs C and D should come together with a pushout diagram $[b]$ performed this time at the level of their labeling graphs. The labeling graph $\mathfrak{s}_S[2]$ produced by the pushout $[b]$ is easy to compute: its vertices are the machine states $s \in \text{State}$ and there is a pair of edges noted $inst_l, inst_r : s \rightarrow s'$ for every edge $inst : s \rightarrow s'$ of the graph $\mathfrak{s}_S[1]$. The intuition is that the code C performs the instructions of the form $inst_l$ in $\mathfrak{s}_S[2]$ while the code D performs the instructions of the form $inst_r$. The labeling graph $\mathfrak{s}_S[2]$ comes together with a relabeling map

$$\mu : \mathfrak{s}_S[2] \longrightarrow \mathfrak{s}_S[1] \quad (17)$$

which maps every edge of the form $inst_l, inst_r : s \rightarrow s'$ of $\mathfrak{s}_S[2]$ to the underlying edge $inst : s \rightarrow s'$ of $\mathfrak{s}_S[1]$. The transition system $(C; D, \lambda_{C; D})$ defining the sequential composition is simply obtained by relabeling along μ the transition system $(C; D, \lambda_{po})$ computed by the pushout of (C, λ_C) and (D, λ_D) in **Trans**, in order to obtain the transition system with labeling map:

$$C; D \xrightarrow{\lambda_{C; D}} \mathfrak{s}_S[1] = C; D \xrightarrow{\lambda_{po}} \mathfrak{s}_S[2] \xrightarrow{\mu} \mathfrak{s}_S[1]$$

The construction establishes that:

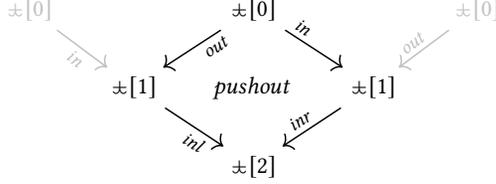
$$\boxed{\text{Sequential composition} = \text{gluing by pushout} + \text{relabeling}} \quad (18)$$

Template games and internal opcategories. The fact that the relabeling along $\mu : \mathfrak{s}_S[2] \rightarrow \mathfrak{s}_S[1]$ plays such a critical role in the definition of sequential composition $C, D \mapsto C; D$ is reminiscent of a similar observation in the work by Melliès on template games for differential linear logic (DiLL). In that case, the construction of the bicategory $\mathbf{Games}(\mathfrak{s})$ of template games, strategies and simulations relies on the assumption that the synchronization template \mathfrak{s} defines an *internal category* in the underlying category \mathbb{S} . An important fact observed for the first time by Bénabou [1] is that an internal category \mathfrak{s} in a category \mathbb{S} with pullbacks is the same thing as a monad in the associated bicategory $\mathbf{Span}(\mathbb{S})$ of spans, see [7] for a discussion. Since we are working in a category \mathbb{S} with pushouts, it makes sense to dualize the situation, and to call **internal opcategory**

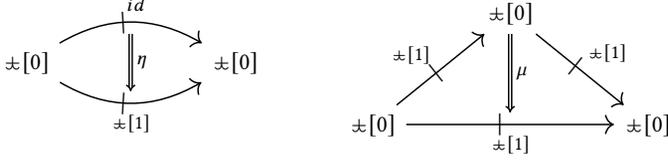
\multimap a monad in the bicategory $\mathbf{Cospan}(\mathbb{S})$. In other words, an internal opcategory \multimap is defined as a pair of objects $\multimap[0]$ and $\multimap[1]$ equipped with a cospan and a pair of morphisms:

$$\multimap[0] \xrightarrow{\text{in}} \multimap[1] \xleftarrow{\text{out}} \multimap[0] \quad \multimap[0] \xrightarrow{\eta} \multimap[1] \quad \multimap[2] \xrightarrow{\mu} \multimap[1] \quad (19)$$

where the object $\multimap[2]$ is defined as the result of the pushout diagram in \mathbb{S} :



In addition, a number of diagrams are required to commute, in order to ensure that the two morphisms η and μ define the following 2-cells in the bicategory $\mathbf{Cospan}(\mathbb{S})$



and that these 2-cells satisfy the unitality and associativity axioms required of a monad in the bicategory $\mathbf{Cospan}(\mathbb{S})$. Note in particular that the requirement that η defines the left hand-side 2-cell is very strong, since it implies that the three morphisms $\eta, \text{in}, \text{out} : \multimap[0] \rightarrow \multimap[1]$ are equal.

A bicategory of cobordisms. We have seen earlier that the labeling graphs $\multimap_S[0] = \text{State}$ and $\multimap_S[1]$ of machine states are equipped with morphisms η and μ explicated in (12) and (17). Somewhat surprisingly, this additional structure happens to be tightly connected to the work by Mellès on template games, as established by the following theorem:

THEOREM 1.1. *The cospan*

$$\multimap_S[0] \xrightarrow{\multimap_S[1]} \multimap_S[0] = \multimap_S[0] \xrightarrow{\eta} \multimap_S[1] \xleftarrow{\eta} \multimap_S[0]$$

together with the graph morphisms η and μ defines an internal opcategory \multimap in the category \mathbf{Gph} .

This theorem means that the machine model \multimap_S is regulated by the very same algebraic principles as the synchronization templates exhibited by Mellès in order to generate his game semantics of differential linear logic (DiLL). The only difference (it is a key difference though) is that the original internal category \multimap in DiLL is “dualized” and replaced in the present situation by an internal opcategory $\multimap = \multimap_S$ of machine states. Guided by this auspicious connection, we associate a bicategory $\mathbf{Cob}(\multimap)$ of **games**, **cobordisms** and **simulations** to every internal opcategory \multimap living in a category \mathbb{S} with pushouts. The terminology of *cobordism* pays tribute to our main source of inspiration for the idea of cospan, which is the construction of the *category of cobordisms* in algebraic topology and in topological quantum field theory, see [20]. A **game** (A, λ_A) is a pair

$$\lambda_A : A \longrightarrow \multimap[0] \quad (20)$$

A **cobordism** $\sigma : A \dashrightarrow B$ between two such games is a quadruple $(S, in, out, \lambda_\sigma)$ consisting of an object S and three morphisms making the diagram below commute:

$$\begin{array}{ccccc}
 A & \xrightarrow{in} & S & \xleftarrow{out} & B \\
 \lambda_A \downarrow & & \downarrow \lambda_\sigma & & \downarrow \lambda_B \\
 \mathfrak{s}[0] & \xrightarrow{\eta} & \mathfrak{s}[1] & \xleftarrow{\eta} & \mathfrak{s}[0]
 \end{array} \tag{21}$$

Here, the object S is called the **support** of the cobordism σ . A **simulation** between two cobordisms

$$\varphi : \sigma \Longrightarrow \tau : A \dashrightarrow B$$

is defined as a morphism $\varphi : S \rightarrow T$ of the original category \mathfrak{S} , making the three diagrams commute:

$$\begin{array}{ccc}
 \begin{array}{ccc} & A & \\ in \swarrow & & \searrow in \\ S & \xrightarrow{\varphi} & T \end{array} &
 \begin{array}{ccc} & B & \\ out \swarrow & & \searrow out \\ S & \xrightarrow{\varphi} & T \end{array} &
 \begin{array}{ccc} S & \xrightarrow{\varphi} & T \\ \lambda_\sigma \swarrow & & \searrow \lambda_\tau \\ & \mathfrak{s}[1] & \end{array}
 \end{array}$$

The composition of cobordisms $\sigma : A \dashrightarrow B$ and $\tau : B \dashrightarrow C$ is defined by pushout and relabeling:

$$\begin{array}{ccccc}
 & A & & B & & C \\
 & \swarrow in & & \swarrow in & & \swarrow out \\
 \lambda_A \downarrow & & & \downarrow \lambda_B & & \downarrow \lambda_C \\
 \mathfrak{s}[0] & & S & & T & & \mathfrak{s}[0] \\
 & \searrow \eta & & \swarrow \eta & & \searrow \eta & \\
 & & \mathfrak{s}[1] & & S+B & & T & & \mathfrak{s}[1] \\
 & & \downarrow \lambda_\sigma & & \downarrow \lambda_{po} & & \downarrow \lambda_\tau & & \\
 & & \mathfrak{s}[1] & & \mathfrak{s}[2] & & \mathfrak{s}[1] & & \\
 & & \downarrow \mu & & & & & & \\
 & & \mathfrak{s}[1] & & & & & &
 \end{array} \tag{22}$$

while the identity cobordism $id_A : A \dashrightarrow A$ is defined as follows:

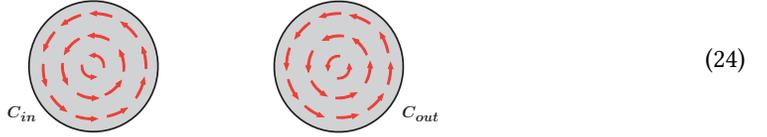
$$\begin{array}{ccccc}
 A & \xrightarrow{id_A} & A & \xleftarrow{id_A} & A \\
 \lambda_A \downarrow & & \downarrow \lambda_A & & \downarrow \lambda_A \\
 \mathfrak{s}[0] & \xrightarrow{in} & \mathfrak{s}[1] & \xleftarrow{out} & \mathfrak{s}[0]
 \end{array} \tag{23}$$

Given a category \mathfrak{S} with pushouts, we establish that

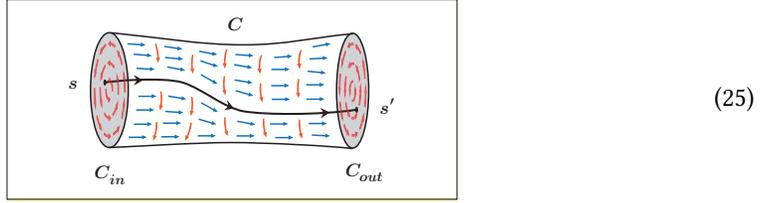
THEOREM 1.2. *Every internal opcategory \mathfrak{s} in the category \mathfrak{S} defines a bicategory $\mathbf{Cob}(\mathfrak{s})$.*

The construction of the bicategory $\mathbf{Cob}(\mathfrak{s})$ of cobordisms is surprisingly similar to the construction of the bicategory $\mathbf{Games}(\mathfrak{s})$ performed by Mellies [16]. In particular, the composition of strategies in $\mathbf{Games}(\mathfrak{s})$ and of cobordisms in $\mathbf{Cob}(\mathfrak{s})$ relies in both cases on a relabeling along the “multiplication” morphism $\mu : \mathfrak{s}[2] \rightarrow \mathfrak{s}[1]$, while the definition of identities relies on the “unit” morphism $\eta : \mathfrak{s}[0] \rightarrow \mathfrak{s}[1]$. There is a simple conceptual explanation for the similitude however, which is that we construct in both cases the bicategory $\mathbf{Games}(\mathfrak{s})$ and $\mathbf{Cob}(\mathfrak{s})$ as a bicategory *sliced above* a formal monad \mathfrak{s} living either in the bicategory $\mathbf{Span}(\mathfrak{s})$ (in the case of games) or in the bicategory $\mathbf{Cospan}(\mathfrak{s})$ (in the case of cobordisms), see [7] for details.

Parallel product as pullback + relabeling. We have just described how two cobordisms $\sigma : A \dashrightarrow B$ and $\tau : B \dashrightarrow C$ can be “glued” together and composed sequentially using a pushout diagram on their common border (B, λ_B) . Similarly, given two cobordisms $\sigma : A \dashrightarrow B$ and $\tau : C \dashrightarrow D$ interpreting concurrent imperative programs, we would like to give a nice and conceptual description of their parallel product $\sigma \parallel \tau : A \parallel C \dashrightarrow B \parallel D$. To that purpose, we shift from a *sequential* to a *concurrent* setting by extending our original labeling graphs $\mathfrak{A}_S[0]$ and $\mathfrak{A}_S[1]$ with transitions performed not just by the Code, but also by the Frame (or the Environment). When compared to the sequential situation of (2), (3) and (4), this extension of the original labeling graphs $\mathfrak{A}_S[0]$ and $\mathfrak{A}_S[1]$ conveys the intuition that a concurrent imperative code C connects an input graph C_{in} to an output graph C_{out} whose only transitions (depicted below in red) are performed by the Frame:



and whose cylinder or cobordism $C : C_{in} \dashrightarrow C_{out}$ admits transitions performed either by the Code (in blue) or by the Frame (in red), as shown below:



The flexibility of our approach based on cobordism is illustrated by the great ease in which the shift of paradigm from sequential to concurrent is performed by moving from the original *one-player* machine model to a *two-player* machine model where transitions may be performed by C (for the Code) or F (for the Frame). In particular, as it stands, the interpretation of the code C as a cobordism is described by the very same diagram (11) as in the sequential case, except that transitions in $\mathfrak{A}[0]$ are now performed by Frame and those in $\mathfrak{A}[1]$ by Frame and by Code. Finally, in order to reflect the asynchronous structure of interactions, we take this opportunity to upgrade our model, and shift from the ambient category $\mathfrak{S} = \mathbf{Gph}$ to the category $\mathfrak{S} = \mathbf{AsynGph}$ of asynchronous graphs, described in §3.

At this stage, another striking connection emerges between our bicategory $\mathbf{Cob}(\mathfrak{A})$ of template games and cobordisms and the original work by Mellès on template games for differential linear logic (DiLL). Indeed, it appears that the parallel product $A \parallel B$ and $\sigma \parallel \tau$ of template games and cobordisms in $\mathbf{Cob}(\mathfrak{A})$ can be defined using the same principles as the tensor product $A \otimes B$ and $\sigma \otimes \tau$ of template games and strategies in the bicategory $\mathbf{Games}(\mathfrak{A})$ for DiLL. The general recipe is to equip the internal opcategory \mathfrak{A} with a pair of internal functors

$$\mathfrak{A} \times \mathfrak{A} \xleftarrow{\text{pick}} \mathfrak{A} \parallel \xrightarrow{\text{pince}} \mathfrak{A} \quad (26)$$

from which one derives (see §4) a lax double functor $\text{pull}[\text{pick}]$ and a pseudo functor $\text{push}[\text{pince}]$ between bicategories, see [10] for definitions:

$$\mathbf{Cob}(\mathfrak{A} \times \mathfrak{A}) \xrightarrow{\text{pull}[\text{pick}]} \mathbf{Cob}(\mathfrak{A} \parallel) \xrightarrow{\text{push}[\text{pince}]} \mathbf{Cob}(\mathfrak{A}) \quad (27)$$

obtained by *pulling along pick* and by *pushing along pince*, respectively. The parallel product

$$\parallel : \mathbf{Cob}(\pm) \times \mathbf{Cob}(\pm) \xrightarrow{m_{\pm, \pm}} \mathbf{Cob}(\pm \times \pm) \longrightarrow \mathbf{Cob}(\pm)$$

is then defined as the lax double functor obtained by precomposing (27) with the canonical pseudo functor

$$m_{\pm_1, \pm_2} : \mathbf{Cob}(\pm_1) \times \mathbf{Cob}(\pm_2) \longrightarrow \mathbf{Cob}(\pm_1 \times \pm_2)$$

The definition is elegantly conceptual, but not entirely transparent, and thus probably worth explicating. The parallel product of two template games A and B is defined by computing the pullback of $\lambda_A \times \lambda_B$ along $\text{pick}[0]$, and then relabeling the resulting labeling map λ_{pb} along $\text{pince}[0]$:

$$\begin{array}{ccc} A \times B & \longleftarrow & A \parallel B \\ \lambda_A \times \lambda_B \downarrow & \text{pullback} & \downarrow \lambda_{pb} \\ \pm[0] \times \pm[0] & \xleftarrow{\text{pick}[0]} & \pm \parallel [0] \xrightarrow{\text{pince}[0]} \pm[0] \end{array} \quad (28)$$

$\lambda_{A \parallel B}$ (curved arrow from $A \parallel B$ to $\pm \parallel [0]$)

In exactly the same way, the parallel product of $\sigma \parallel \tau : A \dashv\vdash B \parallel D$ of two cobordisms $\sigma : A \dashv\vdash B$ and $\tau : C \dashv\vdash D$ with respective supports S and T has its support $S \parallel T$ computed by the following pullback along $\text{pick}[1]$, with resulting labeling map λ_{pb} postcomposed with $\text{pince}[1]$:

$$\begin{array}{ccc} S \times T & \longleftarrow & S \parallel T \\ \lambda_\sigma \times \lambda_\tau \downarrow & \text{pullback} & \downarrow \lambda_{pb} \\ \pm[1] \times \pm[1] & \xleftarrow{\text{pick}[1]} & \pm \parallel [1] \xrightarrow{\text{pince}[1]} \pm[1] \end{array} \quad (29)$$

$\lambda_{\sigma \parallel \tau}$ (curved arrow from $S \parallel T$ to $\pm \parallel [1]$)

As explained in §4, the opcategory $\pm_S \parallel$ describes a *three-player* machine model where every machine transition may be performed by one of the three players C_1 , C_2 , F (for Frame) involved in the parallel product $C_1 \parallel C_2$. The functor pick describes how the machine model $\pm_S \parallel$ can be mapped to a pair $\pm_S \times \pm_S$ of two-player machine models, where C_1 identifies the transitions of C_2 as part of its frame F in the left-hand component \pm_S , and C_2 identifies the transitions of C_1 as part of its frame F in the right-hand component \pm_S . The functor pince then identifies the transitions of the players C_1 and of C_2 in the three-player machine model $\pm_S \parallel$ as the transitions of the code C in the original two-player machine model \pm_S .

To summarize, we see in (28) and (29) that the definition of the parallel product is performed by a pullback along the functor pick , whose purpose is to synchronize the transitions performed by C_1 , C_2 and F , followed by a relabeling along the functor pince . The construction thus establishes that:

$$\boxed{\text{Parallel product} = \text{synchronizing by pullback} + \text{relabeling}} \quad (30)$$

The fact that the sequential composition is computed by a pushout and thus a colimit (18) while the parallel product is computed by a pullback and thus a limit (30) has the immediate and remarkable consequence that there exists a natural and coherent family of morphisms

$$\text{Hoare}_{C_1, C_2, D_1, D_2} : (C_1 \parallel C_2); (D_1 \parallel D_2) \Rightarrow (C_1; D_1) \parallel (C_2; D_2)$$

which turns $\mathbf{Cob}(\pm)$ into a lax monoidal bicategory. This lax monoidal structure of $\mathbf{Cob}(\pm)$ provides a nice algebraic explanation for the Hoare inequality principle [12], since we derive it from the fact that a colimit (sequential composition) always commutes with a limit (parallel product) up to a (in that case non reversible) coercion morphism.

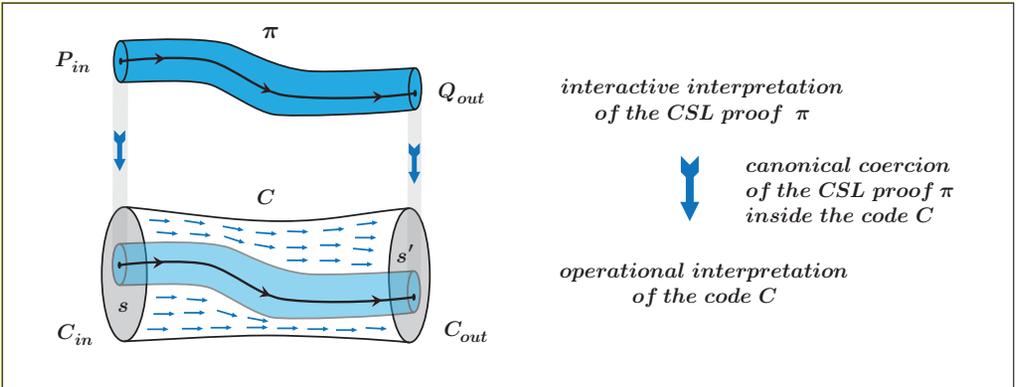
A uniform interpretation of the CSL codes and proofs. One main purpose of the present paper is to reunderstand in a more foundational and axiomatic way the *asynchronous soundness theorem* of concurrent separation logic (CSL) recently established by Mellès and Stefanescu [19]. Their proof of soundness is based on the construction of a game-theoretic and asynchronous interpretation of the codes and of the proofs of CSL in its original form, see Brookes [4]. One main advantage of our approach based on template games is to provide a general, uniform and flexible framework to construct models of CSL, both at the level of codes and of proofs. We will explain in §7 how to construct a machine model $\mathfrak{A}_{\text{Sep}}$ based on the notion of *separated states* introduced by Mellès and Stefanescu [18, 19] in order to interpret the proofs of CSL, and not just the codes as before. We show moreover that there exists a chain of functors between the three machine models or internal opcategories introduced in the paper:

$$\mathfrak{A}_{\text{Sep}} \xrightarrow{u_{\mathcal{F}}} \mathfrak{A}_S \xrightarrow{u_{\mathcal{L}}} \mathfrak{A}_L.$$

As it turns out, given a CSL proof π of a Hoare triple $\{P\}C\{Q\}$, this chain of functors induces a chain of translations

$$\llbracket \pi \rrbracket_{\text{Sep}} \xrightarrow{\mathcal{F}} \llbracket C \rrbracket_S \xrightarrow{\mathcal{L}} \llbracket C \rrbracket_L$$

between the interpretations of the CSL proof π and of the specified code C in the three template game models associated to $\mathfrak{A}_{\text{Sep}}$, \mathfrak{A}_S and \mathfrak{A}_L . The translation from the cobordism $\llbracket \pi \rrbracket_{\text{Sep}}$ interpreting the CSL proof π above the machine model $\mathfrak{A}_{\text{Sep}}$ of separated states to the cobordism $\llbracket C \rrbracket_S$ providing the operational semantics of the code C can be depicted as follows:



In the picture, the blue cylinder above describes the asynchronous graph $\llbracket \pi \rrbracket_{\text{Sep}}$ of separated states where the code C starting from a state $s \in C_{in}$ of the Code satisfying the predicate P will remain and produce an output $s' \in C_{out}$ satisfying the predicate Q , as long as the Frame (or Environment) does not alter the part of the separated state owned by the Code, and respects the invariants required by the CSL judgment. The interpretation of CSL proofs in the machine model $\mathfrak{A}_{\text{Sep}}$ of separated states requires to equip the internal opcategory $\mathfrak{A}_{\text{Sep}}$ with one asynchronous graph $\mathfrak{A}_{\text{Sep}}[0, P]$ for each predicate P of the logic. For that reason, $\mathfrak{A}_{\text{Sep}}$ defines what we call a *colored J -opcategory* where J denotes here the set of predicates of the logic. Technically speaking, $\mathfrak{A}_{\text{Sep}}$ defines a *polyad* in the sense of Bénabou [1] instead of just a monad in the bicategory $\mathbf{Cospan}(\mathbb{S})$ for $\mathbb{S} = \mathbf{AsynGph}$. We explain in §2 how to adapt the construction of the bicategory $\mathbf{Cob}(\pm)$ discussed in the introduction, to the case of a polyad \pm instead of a monad. We explain at the end of the paper, see §10, how this conceptual toolbox sheds light on the constructions underlying the recent proof of asynchronous soundness established by Mellès and Stefanescu [19].

Synopsis. We start by introducing in §2 the notion of internal *colored* opcategory \pm , and explain how to associate a double category $\mathbf{Cob}(\pm)$ of games, cobordisms and simulations to every such synchronization template. Then, we exhibit in §3 two specific internal opcategories \pm_S and \pm_L living in the category $\mathbf{AsynGph}$ of asynchronous graphs, and providing machine models based, respectively, on a *stateful* and *stateless* account of the machine instructions. We then explain in §4 and §5 how to perform the parallel product and the sequential composition in our template game model. This includes the description in §6 of a convenient monadic treatment of errors in our asynchronous and multi-player transition systems. Once the synchronization template \pm_{Sep} of separated state has been recalled in §7, we develop in §8 an axiomatic and fibrational account of lock acquisition and release. This categorical framework enables us to interpret in §9 codes and CSL proofs in a nice and uniform way. We explain in §10 how to derive from our framework the asynchronous soundness theorem recently established in [19]. We discuss the related works in §11 and conclude in §12.

2 THE DOUBLE CATEGORY $\mathbf{Cob}(\pm)$ OF GAMES AND COBORDISMS

We have explained in the introduction how to associate a bicategory of template games and cobordisms to an internal opcategory \pm in a category \mathbb{S} with pushouts. We show here that we can in fact associate to \pm a double category $\mathbf{Cob}(\pm)$ instead of just a bicategory, and extend the construction to the case of *polyads* in $\mathbf{Cospan}(\mathbb{S})$ instead of just monads. We start by recalling the definition of double category, and then of polyad, introduced by Bénabou [1].

2.1 Double categories

We recall the notion of **(pseudo) double category**, which was introduced by Ehresmann [8]. Formally, a double category \mathcal{D} is a weakly internal category in \mathbf{Cat} , the 2-category of small categories. More concretely, it is the data of: a collection of objects, a collection vertical morphisms between objects denoted with a simple arrow $A \rightarrow B$, a collection of horizontal morphisms between pairs of objects denoted with a crossed arrow $A \dashrightarrow B$, and of 2-cells filling squares of the form:

$$\begin{array}{ccc} A_1 & \xrightarrow{F} & B_1 \\ f \downarrow & \Downarrow \alpha & \downarrow g \\ A_2 & \xrightarrow{G} & B_2 \end{array}$$

A 2-cell is called **special** if it is globular, in that f and g are identities. Composition of vertical arrows is associative, whereas composition of horizontal arrows are only associative up to special invertible 2-cells.

The intuition is that, as for most examples, vertical morphisms are the usual notion of morphisms associated to the objects, for example ring morphisms for the double category of rings, and the vertical morphisms are relational structures, such are bimodules in the case of rings. The other canonical example, which is relevant for this paper is the double category of cospans: Given a category \mathbb{S} with pushouts, we consider the double category whose objects and vertical morphisms are respectively the objects and the morphisms of \mathbb{S} , and whose horizontal morphisms between A and B are the cospans in \mathbb{S} of the form depicted below left. A two cell is given by the morphism $S \rightarrow S'$ in the right diagram.

$$\begin{array}{ccc} A & \longrightarrow & S & \longleftarrow & B \\ \downarrow & & \downarrow & & \downarrow \\ A' & \longrightarrow & S' & \longleftarrow & B' \end{array}$$

Composition of horizontal morphisms is given by pushout. Because pushouts are only unique up to iso, given a choice of pushouts, it is easy to check that the universality of pushouts implies that horizontal composition is indeed associative up to an invertible special two cell.

2.2 Polyads

Definition 2.1. The forgetful functor $|-| : \mathbf{Cat} \rightarrow \mathbf{Set}$ which transports every small category to its set of objects has a right adjoint $\mathbf{chaos} : \mathbf{Set} \rightarrow \mathbf{Cat}$ which transports every set J to its **chaotic category** defined as the category $\mathbf{chaos}(J)$ whose objects are the elements i, j, k of the set J , with a unique morphism between each pair of objects. A **polyad** in a bicategory \mathcal{D} is a lax double functor of double category

$$\mathbf{chaos}(J) \longrightarrow \mathcal{D}$$

from the chaotic category over some set J , seen as a double category, to \mathcal{D} . A polyad is called a **monad** when J is a singleton.

It is worth mentioning that a polyad \mathfrak{A} in a bicategory \mathcal{W} is the same thing as an enriched category \mathfrak{A} over the bicategory \mathcal{W} in the terminology of the Australian school [24]. Given a category \mathbb{S} with pushouts, we call **internal J -opcategory** a polyad \mathfrak{A} in the double category $\mathbf{Cospan}(\mathbb{S})$. The definition can be expounded as follows. An internal J -opcategory \mathfrak{A} consists of an object $\mathfrak{A}[0, i]$ of \mathbb{S} for each element $i \in J$, and of a cospan

$$\mathfrak{A}[1, ij] : \mathfrak{A}[0, i] \rightarrow \mathfrak{A}[0, j] = \mathfrak{A}[0, i] \xrightarrow{\text{in}_{ij}} \mathfrak{A}[1, ij] \xleftarrow{\text{out}_{ij}} \mathfrak{A}[0, j]$$

for each pair $i, j \in J$, together with two coherent families η and μ of 2-cells between cospans:

More explicitly, μ_{ijk} is given by the map g_{ijk} in the following commutative diagram:

Finally, there are some conditions about the associativity of the composition, and about the identity; they can be found in [1, def. 5.5.1]. An **internal opcategory** \mathfrak{A} is defined as an internal J -opcategory where the set J is a singleton $J = \{j\}$. We write in that case $\mathfrak{A}[0]$ and $\mathfrak{A}[1]$ for the objects $\mathfrak{A}[0, j]$ and $\mathfrak{A}[1, jj]$ of the ambient category \mathbb{S} , respectively.

2.3 The double category $\mathbf{Cob}(\mathfrak{A})$ of games and cobordisms

Suppose given an internal J -opcategory \mathfrak{A} in a category \mathbb{S} with pushouts. A **j -colored game** (A, λ_A) is defined as an object A of \mathbb{S} equipped with a morphism $\lambda_A : A \rightarrow \mathfrak{A}[0, j]$. An **ij -colored cobordism** from a i -colored game (A, λ_A) to a j -colored game (B, λ_B) is defined as a cospan in \mathbb{S}

together with colors $i, j \in J$ and three maps $\lambda_A, \lambda_B, \lambda_\sigma$ such that the following diagram commutes:

$$\begin{array}{ccccc}
 A & \xrightarrow{\text{in}} & S & \xleftarrow{\text{out}} & B \\
 \lambda_A \downarrow & & \downarrow \lambda_\sigma & & \downarrow \lambda_B \\
 \mathfrak{z}[0, i] & \xrightarrow{\text{in}_{ij}} & \mathfrak{z}[1, ij] & \xleftarrow{\text{out}_{ij}} & \mathfrak{z}[0, j]
 \end{array}$$

As in the case (22) of an internal opcategory, two such ij -colored cobordism $\sigma : A \dashrightarrow B$ and jk -colored cobordism $\tau : B \dashrightarrow C$ can be composed into an ik -colored cobordism $\sigma; \tau : A \dashrightarrow C$ using a pushout and a relabeling along the 2-cell μ_{ijk} provided by the internal J -opcategory, as shown below:

$$\begin{array}{ccccc}
 A & \xrightarrow{\quad C \quad} & B & \xrightarrow{\quad C' \quad} & C \\
 \downarrow & & \downarrow & & \downarrow \\
 \mathfrak{z}[0, i] & \xrightarrow{\quad \mathfrak{z}[1, ij] \quad} & \mathfrak{z}[0, j] & \xrightarrow{\quad \mathfrak{z}[1, jk] \quad} & \mathfrak{z}[0, i] \\
 & \searrow & \downarrow \mu_{ijk} & \nearrow & \\
 & & \mathfrak{z}[1, ik] & &
 \end{array}$$

In order to give cobordisms over an internal J -opcategory \mathfrak{z} a structure of double category, we need identities, which are given by:

$$\begin{array}{ccc}
 A & \xrightarrow{\mathbb{1}_A} & A \\
 \lambda_A \downarrow & & \downarrow \lambda_A \\
 \mathfrak{z}[0, i] & \xrightarrow{\mathbb{1}_{\mathfrak{z}[0, i]}} & \mathfrak{z}[0, i] \\
 & \searrow & \downarrow \eta_i \\
 & & \mathfrak{z}[1, ii]
 \end{array}$$

The vertical morphisms are the maps $f : A \rightarrow A'$ such that $\lambda_{A'} = \lambda_A \circ f$, and the two-cells are triples of maps $f_{\text{in}} : A \rightarrow A', f_{\text{out}} : B \rightarrow B', f : S \rightarrow S'$ such that the following diagram commutes

$$\begin{array}{ccccc}
 A & \xrightarrow{\text{inc}} & S & \xleftarrow{\text{out}_c} & B \\
 f_{\text{in}} \downarrow & & \downarrow f & & \downarrow f_{\text{out}} \\
 A' & \xrightarrow{\text{inc}'_c} & S' & \xleftarrow{\text{out}'_c} & B'
 \end{array}$$

such that moreover, f_{in} and f_{out} are vertical morphisms, and similarly $\lambda_\sigma = \lambda_{\sigma'} \circ f$. One obtains:

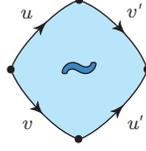
THEOREM 2.2. *Every internal J -opcategory \mathfrak{z} induces a double category $\mathbf{Cob}(\mathfrak{z})$ whose objects are the j -colored games and whose horizontal maps are the cobordisms with composition defined as above.*

3 THE STATEFUL AND STATELESS SYNCHRONIZATION TEMPLATES

We have seen in §2 how to associate a double category $\mathbf{Cob}(\mathfrak{z})$ to every internal J -opcategory \mathfrak{z} living in an ambient category \mathbb{S} with pushouts. As a matter of fact, all the interpretations performed in the present paper will live in the same ambient category $\mathbb{S} = \mathbf{AsynGph}$ of **asynchronous graphs**, which are graphs equipped with two-dimensional tiles. After describing this category in §3.1, we recall in §3.2 the stateful and stateless machine models used in the paper, and simply formulated as asynchronous graphs \mathfrak{z}_S^\bullet and \mathfrak{z}_L^\bullet , along a recipe initiated in [19]. We explain in §3.3 how to derive from \mathfrak{z}_S^\bullet and \mathfrak{z}_L^\bullet the internal opcategories \mathfrak{z}_S and \mathfrak{z}_L living in the ambient category $\mathbb{S} = \mathbf{AsynGph}$ of asynchronous graphs, and associated to the stateful and stateless machine models, respectively.

3.1 The category of asynchronous graphs

A **graph** $G = (V, E, \partial^-, \partial^+)$ consists of a set V of vertices or nodes, a set of E of edges or transitions, and a source and target functions $\partial^-, \partial^+ : E \rightarrow V$. We call a **square** a pair (f, g) of paths $P \rightarrow Q$ of length 2, with the same source and target vertices. An **asynchronous graph** (G, \diamond) is a graph G equipped with a tuple $\diamond = (T, \partial_\diamond, \sigma)$ of a set T of **permutation tiles**, a function ∂_\diamond from the set T to the set of squares of the graph G , and an endofunction σ on T such that if a tile $t \in T$ is mapped by ∂_\diamond to a square (f, g) , then $\partial_\diamond(\sigma(t)) = (g, f)$. We call the function σ the symmetry on \diamond . A tile which is mapped to a square $(u \cdot v', v \cdot u')$ is depicted as a 2-dimensional tile between the paths $f = u \cdot v'$ and $g = v \cdot u'$ as follows:



(31)

The intuition conveyed by such a permutation tile $t : u \cdot v' \diamond v \cdot u'$ is that the two transitions u and v are independent. For that reason, the two paths $u \cdot v'$ and $v \cdot u'$ may be seen as equivalent up to scheduling. Two paths $f, g : M \rightarrow N$ of an asynchronous graph are **equivalent modulo one permutation tile** $h_1 \diamond h_2$ when there exists a tile in T between h_1 and h_2 and when f and g factor as $f = d \cdot h_1 \cdot e$ and $g = d \cdot h_2 \cdot e$ for two paths $d : M \rightarrow P$ and $e : Q \rightarrow N$. We write $f \sim g$ when the path $f : M \rightarrow N$ is “equivalent” to the path $g : M \rightarrow N$ modulo a number of such permutation tiles. Note that the relation \sim is symmetric, reflexive and transitive, and thus defines an equivalence relation, closed under composition.

Definition 3.1. An **asynchronous graph homomorphism**, or **asynchronous morphism**,

$$\mathcal{F} : (G, \diamond_G) \longrightarrow (H, \diamond_H) \quad (32)$$

is a graph homomorphism $\mathcal{F} : G \rightarrow H$ between the underlying graphs, together with a function \mathcal{F}_\diamond from T_G to T_H such that $\mathcal{F} \circ \partial = \partial \circ \mathcal{F}_\diamond$, where we extend \mathcal{F} in the usual way to paths and squares.

This defines the category **AsynGph** of asynchronous graphs and asynchronous morphisms. As it is needed for the cobordism construction, this category has all small limits and colimits, since it is a presheaf category, as is shown below. The limits and colimits are computed pointwise for nodes, edges and tiles. We detail below the case of pullbacks, and consequently of Cartesian products. The pullback of a cospan

$$A_1 \xrightarrow{f} B \xleftarrow{g} A_2$$

of asynchronous graphs is defined as the asynchronous graph $A_1 \times_B A_2$ below. Its nodes are the pairs (x_1, x_2) consisting of a node x_1 in A_1 and of a node x_2 in A_2 , such that $f(x_1) = g(x_2)$. Its edges $(u_1, u_2) : (x_1, x_2) \rightarrow (y_1, y_2)$ are the pairs consisting of an edge $u_1 : x_1 \rightarrow y_1$ in A_1 and of an edge $u_2 : x_2 \rightarrow y_2$ in A_2 , such that $f(u_1) = g(u_2)$. In the same way, a tile $(\alpha_1, \alpha_2) : (u_1, u_2) \cdot (v'_1, v'_2) \diamond (v_1, v_2) \cdot (u'_1, u'_2)$ is a pair consisting of a tile $\alpha_1 : u_1 \cdot v'_1 \diamond v_1 \cdot u'_1$ of the asynchronous graph A_1 and of a tile $\alpha_2 : u_2 \cdot v'_2 \diamond v_2 \cdot u'_2$ of the asynchronous graph A_2 , such that the two tiles $f(\alpha_1)$ and $g(\alpha_2)$ are equal in the asynchronous graph B . The Cartesian product $A_1 \times A_2$ of two asynchronous graphs is obtained by considering the special case when B is the terminal asynchronous graph, with one node, one edge, and one tile. The definition of $A_1 \times A_2$ thus amounts to forgetting the equality conditions in the definition of the pullback $A_1 \times_B A_2$ above.

REMARK 3.2. The category **AsynGph** of asynchronous graphs can be seen as the category of presheaves over the category presented by the graph:

$$[0] \begin{array}{c} \xrightarrow{s} \\ \xrightarrow{t} \end{array} [1] \begin{array}{c} \xrightarrow{dl} \\ \xrightarrow{dr} \\ \xrightarrow{ur} \\ \xrightarrow{ul} \end{array} [2] \begin{array}{c} \xrightarrow{\sigma} \\ \xrightarrow{\sigma} \end{array}$$

and with the equations:

$$\begin{array}{llll} dl \circ s = ul \circ s & dl \circ t = dr \circ s & dr \circ t = ur \circ t & ul \circ t = ur \circ s \\ \sigma \circ dl = ul & \sigma \circ ul = dl & \sigma \circ dr = ur & \sigma \circ ur = dr \end{array}$$

3.2 The stateful and stateless machine models

Following [19], we define the stateful and the stateless *machine models* as asynchronous graphs \mathfrak{A}_S^\bullet and \mathfrak{A}_L^\bullet , describing the primitive semantics of the machine: the states, the transitions between them, and the tiles which explain how to permute the order of execution of two transitions performed in parallel. Note that each model depends on an implicit set \mathbb{L} of free locks; we will come back to this point and make it precise in §8.

3.2.1 *The stateful model* \mathfrak{A}_S^\bullet . A **memory state** is an element $\mu = (s, h)$ of the set $(\mathbf{Var} \rightarrow_{fin} \mathbf{Val}) \times (\mathbf{Loc} \rightarrow_{fin} \mathbf{Val})$ with $\mathbb{N} \subseteq \mathbf{Loc} \subseteq \mathbf{Val}$, where the finite partial map s stands for the stack and h for the heap. A **machine state** is a pair $\mathfrak{s} = (\mu, L)$ of a memory state and of a subset of \mathbb{L} , which represents the available locks. A **machine state footprint**

$$\rho \in \mathcal{P}(\mathbf{Var} + \mathbf{Loc}) \times \mathcal{P}(\mathbf{Var} + \mathbf{Loc}) \times \mathcal{P}(\mathbb{L}) \times \mathcal{P}(\mathbf{Loc})$$

is, made of: (i) $\text{rd}(\rho)$, the part of the memory that is *read*, (ii) $\text{wr}(\rho)$, the part of the memory that is *written*, (iii) $\text{lock}(\rho)$, the locks that are *touched*, and (iv) $\text{mem}(\rho)$ the addresses that are *allocated* or *deallocated*. Two footprints ρ and ρ' are declared **independent** when:

$$\begin{array}{ll} (\text{rd}(\rho) \cup \text{wr}(\rho)) \cap \text{wr}(\rho') = \emptyset & \text{lock}(\rho) \cap \text{lock}(\rho') = \emptyset \\ (\text{rd}(\rho') \cup \text{wr}(\rho')) \cap \text{wr}(\rho) = \emptyset & \text{mem}(\rho) \cap \text{mem}(\rho') = \emptyset \end{array}$$

The **stateful model** \mathfrak{A}_S^\bullet is defined as the following asynchronous graph: its nodes are the machine states and $\frac{1}{2}$, its transitions

$$(\mu, L) \xrightarrow{m} (\mu', L') \quad \text{or} \quad (\mu, L) \xrightarrow{m} \frac{1}{2}$$

are given by the semantics of the instructions, which are of the form:

$$m ::= x := E \mid x := [E] \mid [E] := E' \mid \text{test}(B) \mid \text{nop} \mid x := \text{alloc}(E, \ell) \mid \text{dispose}(E) \mid P(r) \mid V(r)$$

where $x \in \mathbf{Var}$ is a variable, $r \in \mathbf{Locks}$ is a resource name, ℓ is a location, and E, E' are arithmetic expressions, possibly with “*free*” variables in \mathbf{Var} . For example, the instruction $x := E$ executed in a machine state $\mathfrak{s} = (\mu, L)$ assigns to the variable x the value $E(\mu) \in \mathbf{Val}$ when the value of the expression E can be evaluated in the memory state μ , and produces the runtime error $\frac{1}{2}$ otherwise. The instruction $P(r)$ acquires the resource variable r when it is available, while the instruction $V(r)$ releases it when r is locked, as described below:

$$\begin{array}{c} \frac{E(\mu) = v}{(\mu, L) \xrightarrow{x:=E} (\mu[x \mapsto v], L)} \\ \frac{r \notin L}{(\mu, L) \xrightarrow{P(r)} (\mu, L \uplus \{r\})} \end{array} \quad \begin{array}{c} \frac{E(\mu) \text{ not defined}}{(\mu, L) \xrightarrow{x:=E} \frac{1}{2}} \\ \frac{r \notin L}{(\mu, L \uplus \{r\}) \xrightarrow{V(r)} (\mu, L)} \end{array}$$

The inclusion $\mathbf{Loc} \subseteq \mathbf{Val}$ means that an expression E may also denote a location. In that case, $[E]$ refers to the value stored at location E in the heap. The instruction $x := \text{alloc}(E, \ell)$ allocates some memory space on the heap at address $\ell \in \mathbf{Loc}$, initializes it with the value of the expression E , and assigns the address ℓ to the variable $x \in \mathbf{Var}$ if ℓ was free, otherwise there is no transition. $\text{dispose}(E)$ deallocates the location denoted by E when it is allocated, and returns $\frac{1}{2}$ otherwise. The instruction nop (for no-operation) does not alter the state. The instruction $\text{test}(B)$ behaves like nop when the initial state satisfies B , and is not defined otherwise. The asynchronous tiles of \mathfrak{A}_S are the squares of the form

$$s \xrightarrow{m} s_1 \xrightarrow{m'} s' \sim s \xrightarrow{m'} s_2 \xrightarrow{m} s'$$

where their footprints are independent in the sense above.

3.2.2 The stateless model \mathfrak{A}_L^\bullet . A lock footprint

$$\rho \in \mathcal{P}(\mathbb{L}) \times \mathcal{P}(\mathbf{Loc})$$

is made of a set of locks $\text{lock}(\rho)$ and a set of locations $\text{mem}(\rho)$. Two such footprints are **independent** when their sets are component-wise disjoint. The **stateless model** \mathfrak{A}_L^\bullet is defined in the following way: its nodes are the subsets of \mathbb{L} , and its transitions are all the edges of the form (note the non-determinism)

$$L \xrightarrow{P(r)} L \uplus \{r\} \quad L \xrightarrow{\text{alloc}(\ell)} L \quad L \xrightarrow{\tau} L \quad L \uplus \{r\} \xrightarrow{V(r)} L \quad L \xrightarrow{\text{dispose}(\ell)} L \quad L \xrightarrow{m} \frac{1}{2}$$

where m is a **lock instruction** of the form:

$$P(r) \mid V(r) \mid \text{alloc}(\ell) \mid \text{dispose}(\ell) \mid \tau$$

for $\ell \in \mathbf{Loc}$ and $r \in \mathbb{L}$. The purpose of these transitions is to extract from each instruction of the machine its synchronization behavior. An important special case, the transition τ represents the absence of any synchronization mechanism in an instruction like $x := E$, $x := [E]$ or $[E] := E'$. The asynchronous tiles of \mathfrak{A}_L are the squares of the form

$$L \xrightarrow{x} L_1 \xrightarrow{y} L' \sim L \xrightarrow{y} L_2 \xrightarrow{x} L'$$

when the lock footprints of x and y are independent. It is worth noting that L' may be equal to $\frac{1}{2}$ in such an asynchronous tile. Note that the asynchronous graph \mathfrak{A}_L^\bullet is more liberal than \mathfrak{A}_S^\bullet about which footprints commute, because it only takes into account the locks as well as the allocated and deallocated locations. As explained in the introduction, this mismatch enables us to detect *data races* in the machine as well as in the code, see also [19].

3.3 The stateful and the stateless internal opcategories \mathfrak{A}_S and \mathfrak{A}_L

We have just described in §3.2 the asynchronous graphs \mathfrak{A}_S^\bullet and \mathfrak{A}_L^\bullet which we take as stateful and stateless machine models in the paper. We explain now how we turn these machine models \mathfrak{A}_S^\bullet and \mathfrak{A}_L^\bullet into the internal opcategories \mathfrak{A}_S and \mathfrak{A}_L which we will use, in the ambient category $\mathbb{S} = \mathbf{AsynGph}$ of asynchronous graphs. The constructions of the internal opcategories $\mathfrak{A} = \mathfrak{A}_S, \mathfrak{A}_L$ from the asynchronous graphs $\mathfrak{A}^\bullet = \mathfrak{A}_S^\bullet, \mathfrak{A}_L^\bullet$ follows exactly the same recipe: both of them are defined as cospans of monomorphisms in the ambient category $\mathbb{S} = \mathbf{AsynGph}$ of asynchronous graphs

$$\mathfrak{A}[0] \xleftarrow{\text{in}} \mathfrak{A}[1] \xleftarrow{\text{out}} \mathfrak{A}[0]$$

describing $\mathfrak{A}^\bullet = \mathfrak{A}_S^\bullet, \mathfrak{A}_L^\bullet$ as a monad (or polyad with a single color $j \in J$) in the double category $\mathbf{Cospan}(\mathbb{S})$, see §2 for details. We like to think of the asynchronous graphs \mathfrak{A}_S^\bullet and \mathfrak{A}_L^\bullet defined in §3.2 as “solipsistic” games with only one player: the underlying machine. Building on this intuition,

we follow the conceptual track discussed in the introduction, and construct an asynchronous graph $\mathfrak{z}[1] = \mathfrak{z}_S[1], \mathfrak{z}_L[1]$ with two players (for Code and Environment) instead of one, both in the stateful case of $\mathfrak{z}[1] = \mathfrak{z}_S[1]$ and in the stateless case $\mathfrak{z}[1] = \mathfrak{z}_L[1]$. The shift from one player to two players is performed in a very simple way. We consider the functor $\Omega : \mathbf{Set} \rightarrow \mathbf{AsynGph}$ which transports a given set \mathcal{L} of labels to the asynchronous graph $\Omega(\mathcal{L})$ with one single node, the elements of \mathcal{L} as edges, and one tile for each square. We are particularly interested in the case when the set of labels $\mathcal{L} = \{C, F\}$ contains the two polarities C and F associated to the Code and to the Frame (or the Environment), respectively. The asynchronous graph $\Omega(\{C, F\})$ enables us to define the *two-player* stateful and stateless machine models $\mathfrak{z}_S^{\circ\bullet}$ and $\mathfrak{z}_L^{\circ\bullet}$ as the Cartesian product of asynchronous graphs

$$\mathfrak{z}_S^{\circ\bullet} = \mathfrak{z}_S^{\bullet} \times \Omega(\{C, F\}) \quad \mathfrak{z}_L^{\circ\bullet} = \mathfrak{z}_L^{\bullet} \times \Omega(\{C, F\})$$

Note that the resulting asynchronous graph $\mathfrak{z}^{\circ\bullet} = \mathfrak{z}_S^{\circ\bullet}, \mathfrak{z}_L^{\circ\bullet}$ has the same nodes as $\mathfrak{z}^{\bullet} = \mathfrak{z}_S^{\bullet}, \mathfrak{z}_L^{\bullet}$ and two edges, the first one labeled with a polarity C for Code, the second one labeled with a polarity F for Frame, for each edge in the original asynchronous graph $\mathfrak{z}^{\bullet} = \mathfrak{z}_S^{\bullet}, \mathfrak{z}_L^{\bullet}$. The two circles \circ and \bullet in the notation $\mathfrak{z}^{\circ\bullet}$ are mnemonics designed to remind us that there are two players in the game: the Code playing the white side (\circ) and the Environment playing the black side (\bullet). We have accumulated enough material at this stage to define the internal opcategories $\mathfrak{z} = \mathfrak{z}_S, \mathfrak{z}_L$ in the ambient category $\mathbb{S} = \mathbf{AsynGph}$. The asynchronous graph $\mathfrak{z}[1]$ is defined as the two-player machine model $\mathfrak{z}^{\circ\bullet}$ while the asynchronous graph $\mathfrak{z}[0]$ is defined as the one-player machine model. In summary:

$$\mathfrak{z}[1] = \mathfrak{z}^{\circ\bullet} \quad \mathfrak{z}[0] = \mathfrak{z}^{\bullet}.$$

The asynchronous graph $\mathfrak{z}[0]$ with one player is then embedded in the asynchronous graph $\mathfrak{z}[1]$ with two players by the homomorphism $\text{in} = \text{out} : \mathfrak{z}[0] \rightarrow \mathfrak{z}[1]$ obtained by transporting every node of $\mathfrak{z}[0]$ to the corresponding node in $\mathfrak{z}[1]$, and every edge of $\mathfrak{z}[0]$ to the corresponding edge in $\mathfrak{z}[1]$ with the polarity F of the Frame.

4 THE PARALLEL PRODUCT

We describe below in full detail how to construct the parallel product $C_1 \parallel C_2$ of two codes C_1 and C_2 by applying a *push* and *pull* functors along a pair (26) of internal functors. We start by formulating in §4.1 the notion of **internal functor** from an internal J -opcategory \mathfrak{z} to an internal J' -opcategory \mathfrak{z}' living in the same ambient category \mathbb{S} with pushouts. We then introduce in §4.3 the notion of *acute span*, which leads us to the notion of *span-monoidal* internal J -opcategory formulated in §4.4. The parallel product of \parallel of two codes is then described in §4.5.

4.1 Internal functors between internal J -opcategories

The notion of internal functor between internal J -opcategories is defined as expected.

Definition 4.1 (Internal functor). An internal functor $F = (f, F[0, \cdot], F[1, \cdot])$ from \mathfrak{z} to \mathfrak{z}' is a triple consisting of a function $f : J \rightarrow J'$ between the sets of colors, together with:

- for each $i \in J$, a map $F[0, i]$ in the ambient category \mathbb{S} :

$$F[0, i] : \mathfrak{z}[0, i] \longrightarrow \mathfrak{z}'[0, f(i)],$$

- for each $i, j \in J$, a map $F[1, ij]$ in the ambient category \mathbb{S} :

$$F[1, ij] : \mathfrak{z}[1, ij] \longrightarrow \mathfrak{z}'[1, f(ij)]$$

where we use the lighter notation $f(ij)$ for $f(i)f(j)$. One asks moreover that the diagram below commutes

$$\begin{array}{ccccc} \mathfrak{A}[0, i] & \xrightarrow{\text{in}_{ij}} & \mathfrak{A}[1, ij] & \xleftarrow{\text{out}_{ij}} & \mathfrak{A}[0, j] \\ F[0, i] \downarrow & & \downarrow F[1, ij] & & \downarrow F[0, j] \\ \mathfrak{A}'[0, f(i)] & \xrightarrow{\text{in}_{f(i)f(j)}} & \mathfrak{A}'[1, f(ij)] & \xleftarrow{\text{out}_{f(i)f(j)}} & \mathfrak{A}'[0, f(j)] \end{array}$$

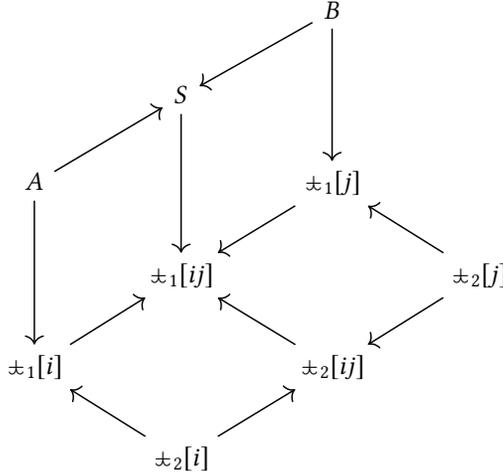
and that the internal functor is compatible with the identities: the diagram on the right below commutes; and with composition: the maps $F[2, ijk] : \mathfrak{A}[2, ijk] \rightarrow \mathfrak{A}'[2, f(ijk)]$ induced by universality of the pushout must make the left diagram commute.

$$\begin{array}{ccc} \mathfrak{A}[2, ijk] & \longrightarrow & \mathfrak{A}[1, ik] \\ F[2, ijk] \downarrow & & \downarrow F[1, ik] \\ \mathfrak{A}'[2, f(ijk)] & \longrightarrow & \mathfrak{A}'[1, f(ik)] \end{array} \qquad \begin{array}{ccc} \mathfrak{A}[0, i] & \xrightarrow{\eta^i} & \mathfrak{A}[1, ik] \\ F[0, i] \downarrow & & \downarrow F[1, ii] \\ \mathfrak{A}[0, f(i)] & \xrightarrow{\eta'_{f(i)}} & \mathfrak{A}'[1, f(ii)] \end{array}$$

The internal J -opcategories and internal functors form a category $\mathbf{opCat}(\mathbb{S})$. This category admits products: the index set of the product of two internal categories is the product of their index sets, and $(\mathfrak{A} \times \mathfrak{A}')[0, (i, j)] = \mathfrak{A}[0, i] \times \mathfrak{A}'[0, j]$. This fact will play a central role in the sequel.

4.2 Plain internal functors

An important family of internal functors are **plain internal functors**, which are functors whose action on the colors is the identity. With the notations of Definition 4.1, they are the internal functors such that $f = id$. Plain internal functors are useful because they define a pullback operation when \mathbb{S} has pullbacks, given by the obvious three pullbacks in the following diagram:



This construction induces a lax double functor between $\mathbf{Cob}(\mathfrak{A}_2)$ and $\mathbf{Cob}(\mathfrak{A}_1)$. The converse operation of pushing exists for any internal functor.

LEMMA 1. *A plain internal functor $F : \mathfrak{A}_1 \rightarrow \mathfrak{A}_2$ induces a lax double functor of double category defined by taking the pointwise pullbacks along the components of F*

$$\text{pull}[F] : \mathbf{Cob}(\mathfrak{A}_2) \longrightarrow \mathbf{Cob}(\mathfrak{A}_1).$$

Conversely, any internal functor $F : \mathfrak{A}_1 \rightarrow \mathfrak{A}_2$ induces a pseudo functor by post-composition

$$\text{push}[F] : \mathbf{Cob}(\mathfrak{A}_1) \longrightarrow \mathbf{Cob}(\mathfrak{A}_2).$$

4.3 Acute spans of internal functors

We start by introducing the notion of **acute span** of internal functors, and then describe how transport of structure works along an acute span.

Acute spans. An **acute span** between an internal J_1 -opcategory \mathfrak{A}_1 and an internal J_2 -opcategory \mathfrak{A}_2 is defined as a span of internal functors

$$\mathfrak{A}_1 \xleftarrow[=]{F} \mathfrak{A}_0 \xrightarrow{G} \mathfrak{A}_2$$

where \mathfrak{A}_0 is an internal J_0 -opcategory and the $=$ sign indicates that the internal functor F is plain. Here, we suppose that the ambient category \mathbb{S} has pushouts and pullbacks. In that case, the definition gives rise to a bicategory **AcuteSpan**, whose objects are the internal J -opcategories in the ambient category \mathbb{S} , whose 1-cells are acute spans, and whose 2-cells are commuting diagrams of the form:

$$\begin{array}{ccc} & \mathfrak{A}_0 & \\ \swarrow \scriptstyle{=} & \downarrow \scriptstyle{\phi} & \searrow \\ \mathfrak{A}_1 & \mathfrak{A}'_0 & \mathfrak{A}_2 \\ \swarrow \scriptstyle{=} & \downarrow & \searrow \\ & \mathfrak{A}_0 & \end{array}$$

where the internal functor $\phi : \mathfrak{A}_0 \rightarrow \mathfrak{A}'_0$ is plain. Acute spans are composed using pullbacks of plain internal functors along internal functors, in the category of internal J -opcategories. The pullback is defined as follows. Assume that we are given two internal functors:

$$\mathfrak{A}_1 \xrightarrow{(f,F)} \mathfrak{A}_2 \xleftarrow[=]{(id,G)} \mathfrak{A}_3$$

between an internal J_1 -opcategory \mathfrak{A}_1 and a J_3 -opcategory \mathfrak{A}_3 . Their pullback is defined as the internal J_1 -opcategory $\mathfrak{A}_1 \times_{\mathfrak{A}_2} \mathfrak{A}_3$ described below. At the level of its components and objects of \mathbb{S} , for each $i \in J_1$, the two internal functors give the following diagram:

$$\mathfrak{A}_1[0, i] \xrightarrow{F[0, i]} \mathfrak{A}_2[0, f(i)] \xleftarrow{G[0, f(i)]} \mathfrak{A}_3[0, f(i)]$$

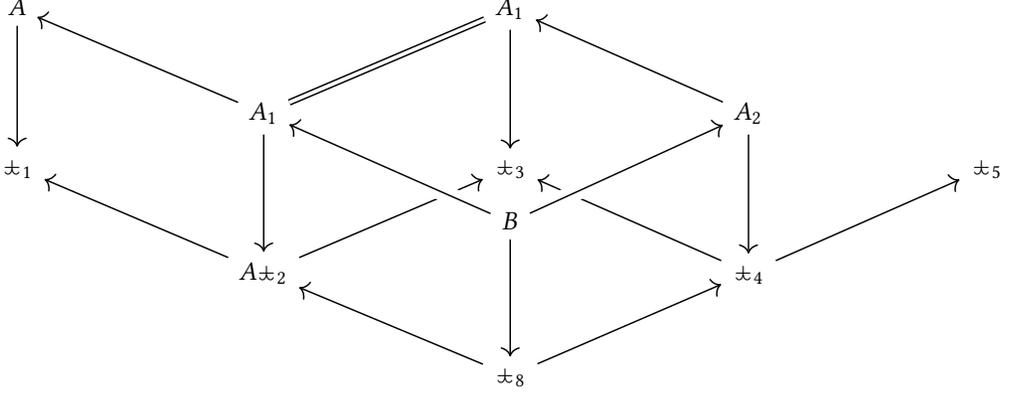
and we simply define $(\mathfrak{A}_1 \times_{\mathfrak{A}_2} \mathfrak{A}_3)[0, i]$ as the pullback of that diagram in the ambient category \mathbb{S} . We proceed similarly to define the $(\mathfrak{A}_1 \times_{\mathfrak{A}_2} \mathfrak{A}_3)[1, ij]$; and the universality of the pullbacks in \mathbb{S} gives the structural maps between the two.

Transport along acute spans. The ultimate *raison d'être* of acute spans is to induce an operation of transport by “pull-then-push” along the two legs of a span. This operation plays a fundamental role in the paper, in particular because we derive our definition of parallel product from it. This operation can be formulated as a pseudo functor $\mathbf{Cob} : \mathbf{AcuteSpan} \rightarrow \mathbf{DbCat}_{\text{lax}}$ of bicategories, from the bicategory **AcuteSpan** just defined to the bicategory $\mathbf{DbCat}_{\text{lax}}$ of double categories and lax double functors. It is defined on objects and 1-cells by:

$$\begin{aligned} \mathbf{Cob} & : \mathbf{AcuteSpan} \longrightarrow \mathbf{DbCat}_{\text{lax}} \\ \mathfrak{A} & \longmapsto \mathbf{Cob}(\mathfrak{A}) \\ (F, G) & \longmapsto \text{push}[G] \circ \text{pull}[F] \end{aligned}$$

To prove that this induces a pseudo functor, one needs in particular to check that, given two composable acute spans F and G , $\mathbf{Cob}(F \circ G) \cong \mathbf{Cob}(F) \circ \mathbf{Cob}(G)$. This is indeed the case because,

in the following commutative diagram, the map from B to A_2 is an isomorphism, for the top face of the cube is a pullback according to the pasting lemma for pullbacks.



4.4 Span-monoidal internal J -opcategories

We use the span operation above to define the parallel product of two programs. Categorically, we construct a lax-monoidal structure on $\mathbf{Cob}(\otimes)$, that is to say a lax double functor

$$\parallel : \mathbf{Cob}(\otimes) \times \mathbf{Cob}(\otimes) \rightarrow \mathbf{Cob}(\otimes).$$

By lax double functor, we mean that there exists a natural and coherent family of maps:

$$(C_1 \parallel C_2); (D_1 \parallel D_2) \longrightarrow (C_1; D_1) \parallel (C_2; D_2).$$

Since the composition of cobordisms corresponds to the sequential composition of programs, these coercion maps capture the famous Hoare inequality [12]

$$(C_1 \parallel C_2); (D_1 \parallel D_2) \subseteq (C_1; D_1) \parallel (C_2; D_2).$$

In our setting, this follows from Proposition 1, and therefore from the fact that there is always a map from a colimit of limits to the corresponding limit of colimits.

This tensor product is built the same way as in the case of template games [16], using the notion of internal span-monoidal opcategory. We remarked below Definition 4.1, the category $\mathbf{opCat}(\mathbb{S})$ of internal opcategories in the ambient category \mathbb{S} is a Cartesian category. As a consequence, the lax functor \mathbf{Cob} is lax monoidal, where we equip both $\mathbf{AcuteSpan}$ and $\mathbf{DblCat}_{\text{lax}}$ with the monoidal structure induced by their Cartesian products. In particular, there exist coercions living in the Cartesian category $\mathbf{DblCat}_{\text{lax}}$, and thus provided by pseudo functors of double categories

$$\begin{array}{lcl} m_{\otimes_1, \otimes_2} & : & \mathbf{Cob}(\otimes_1) \times \mathbf{Cob}(\otimes_2) \longrightarrow \mathbf{Cob}(\otimes_1 \times \otimes_2) \\ m_1 & : & \mathbb{1} \longrightarrow \mathbf{Cob}(\mathbf{1}) \end{array}$$

The first coercion is obtained in the natural way by taking the “pointwise” Cartesian product of the two cobordisms, and the second is the trivial cobordism, given by the initial category $\mathbf{1}$.

Definition 4.2. A **span-monoidal internal J -opcategory** $(\otimes, \parallel, \eta)$ is a symmetric pseudomonoid object in the symmetric monoidal bicategory $\mathbf{AcuteSpan}$. In particular, \parallel and η are two acute spans:

$$\otimes \times \otimes \xleftarrow{\text{pick}} \otimes \parallel \xrightarrow{\text{pince}} \otimes \qquad \mathbf{1} \xleftarrow{\eta} \otimes \xrightarrow{\eta} \otimes.$$

together with invertible 2-cells that witness the associativity of \parallel , and the fact that η is a left and right identity of \parallel . See [5, §3] for the complete definition.

Now, combining the external operations $m_{\pm, \pm}$ and m_1 on the one hand, and the internal operations \parallel, η on the other, we get a lax-monoidal structure on $\mathbf{Cob}(\pm)$ whose multiplication and unit are respectively given by:

$$\begin{array}{ccccc} \mathbf{Cob}(\pm) \times \mathbf{Cob}(\pm) & \xrightarrow{m_{\pm, \pm}} & \mathbf{Cob}(\pm \times \pm) & \xrightarrow{\mathbf{Cob}(\parallel)} & \mathbf{Cob}(\pm) \\ \mathbb{1} & \xrightarrow{m_1} & \mathbf{Cob}(1) & \xrightarrow{\mathbf{Cob}(\eta)} & \mathbf{Cob}(\pm) \end{array}$$

THEOREM 4.3. *Every span-monoidal internal J -opcategory \pm induces a symmetric lax-monoidal double category $\mathbf{Cob}(\pm)$.*

4.5 Illustration: parallel product of codes

Now that we have a general method for equipping the double category $\mathbf{Cob}(\pm)$ with a lax-monoidal structure, we use it to define the parallel product for the stateful and the stateless semantics of the Code. The case of separated states is a bit more involved and will be treated later, see §7.4 for details. The basic idea is to think of the code $C_1 \parallel C_2$ as a situation where (1) there are *three* players involved: the Code of C_1 , the Code of C_2 and the overall Frame F , but where (2) we have forgotten the identities of the codes C_1 and C_2 by considering both of them to be the Code C . This idea leads us to define the three-player machine models $\pm_S^{\circ\circ\bullet}$ and $\pm_L^{\circ\circ\bullet}$, with polarities C_1, C_2 and F . In the same way the two-player versions of the machine models are deduced from their one-player versions in §3.3 using a product, we use here the pullback:

$$\begin{array}{ccc} \pm^{\circ\circ\bullet} & \xrightarrow{\pi_{\text{pol}}^{(12)(F)}} & \Omega\{C_1, C_2, F\} \\ \pi_{\text{state}}^{(12)(F)} \downarrow & & \downarrow \Omega(12)(F) \\ \pm^{\circ\bullet} & \xrightarrow{\pi_{\text{pol}}} & \Omega\{C, F\} \end{array}$$

where $(12)(F)$ denotes the function $\{C_1, C_2, F\} \rightarrow \{C, F\}$ which maps the polarities C_1 and C_2 to C , and the polarity F to itself. More explicitly, $\pm^{\circ\circ\bullet}$ has three copies for each transition of $\pm^{\circ\bullet}$, one copy for each of the three polarities C_1, C_2, F . The span-monoidal structures on the internal opcategories $\pm = \pm_S, \pm_L$ are defined in the same way:

$$\pm^{\circ\bullet} \times \pm^{\circ\bullet} \xleftarrow{\left\langle \pi_{\text{state}}^{(1)(2F)}, \pi_{\text{state}}^{(2)(1F)} \right\rangle} \pm^{\circ\circ\bullet} \xrightarrow{\pi_{\text{state}}^{(12)(F)}} \pm^{\circ\bullet} \quad \mathbb{1} \longleftarrow \pm^{\circ\bullet} \xrightarrow{\iota_F} \pm^{\circ\bullet}.$$

where, for instance, $\pi_{\text{state}}^{(1)(2F)}$ maps every transition with polarity C_1, C_2, F to the corresponding transition with polarity given by the map $C_1 \mapsto C$ and $C_2, F \mapsto F$; while the homomorphism ι_F embeds the asynchronous graph $\pm^{\circ\bullet}$ into $\pm^{\circ\circ\bullet}$ with every edge transported to the corresponding edge of polarity F . The resulting lax-monoidal product \parallel is essentially the same as the parallel product which was defined by hand in Melliès and Stefanescu [19]. The product synchronizes transitions of the Code in C_1 with transitions of the Environment in C_2 which are mapped to the same transition in \pm . The intuition here is that a transition of C_1 is seen by C_2 as a transition of its Environment. Note that the parallel product preserves tiles from C_1 and from C_2 , and adds Code/Code tiles when one transition comes from C_1 and the other transition from C_2 —meaning that the two instructions are executed on two different “threads”—and their image in $\pm^{\circ\bullet} \times \pm^{\circ\bullet}$ forms a tile—meaning that these two instructions are independent.

5 SEQUENTIAL COMPOSITION

So far, to compose cobordisms horizontally, the target of the first cobordism must exactly match the source of the second. This is not a reasonable assumption, for the initial and the final states of

a program is part of its internal state. To summarize, in general, the two cobordisms we wish to compose look like:

$$\begin{array}{ccc}
 A & \xrightarrow{\quad} & B \\
 \downarrow & & \downarrow \lambda_{\text{out}} \\
 \mathfrak{s}[0, i] & \xrightarrow{\mathfrak{s}[1, ij]} & \mathfrak{s}[0, j]
 \end{array}
 \qquad
 \begin{array}{ccc}
 B' & \xrightarrow{\quad} & C' \\
 \lambda_{\text{in}} \downarrow & & \downarrow \\
 \mathfrak{s}[0, i'] & \xrightarrow{\mathfrak{s}[1, i'j']} & \mathfrak{s}[0, j']
 \end{array}$$

In practice, in all the cases we consider, $\mathfrak{s}[0, j]$ and $\mathfrak{s}[0, i']$ will be equal, but B and B' will be different. To bridge the gap between $(B, \lambda_{\text{out}})$ and $(B', \lambda_{\text{in}})$, we will use a **filling system** over the internal J -opcategory \mathfrak{s} . To each pair of interfaces $\lambda_{\text{out}} : B \rightarrow \mathfrak{s}[0, j]$ and $\lambda_{\text{in}} : B' \rightarrow \mathfrak{s}[0, i']$, the filling system associates a cobordism

$$(B, \lambda_{\text{out}}) \longrightarrow \text{fill}((B, \lambda_{\text{out}}), (B', \lambda_{\text{in}})) \longleftarrow (B', \lambda_{\text{in}})$$

from $(B, \lambda_{\text{out}})$ to $(B', \lambda_{\text{in}})$. Thanks to this mediating cobordism, it becomes possible to compose the two cobordisms using the usual composition of cobordism. Given such a filling system, we write $C_1 \mathbin{\text{\textcircled{;}}} C_2$ for this generalized form of composition.

PROPOSITION 1. *Suppose that there exists a map $\text{fill}(\lambda \parallel \lambda', \mu \parallel \mu') \rightarrow \text{fill}(\lambda, \mu) \parallel \text{fill}(\lambda', \mu')$. In that case, the Hoare inequality holds: $(C_1 \parallel C'_1) \mathbin{\text{\textcircled{;}}} (C_2 \parallel C'_2) \rightarrow (C_1 \mathbin{\text{\textcircled{;}}} C_2) \parallel (C'_1 \mathbin{\text{\textcircled{;}}} C'_2)$.*

PROOF. The Hoare inequality for the usual composition applied twice gives us the map below, from which we can conclude using the hypothesis on the filling system.

$$(C_1 \parallel C'_1); (\text{fill}(\lambda, \mu) \parallel \text{fill}(\lambda', \mu')); (C_2 \parallel C'_2) \rightarrow (C_1 \mathbin{\text{\textcircled{;}}} C_2) \parallel (C'_1 \mathbin{\text{\textcircled{;}}} C'_2) \quad \square$$

When the base category \mathfrak{S} has pullbacks as well as pushouts, which is the case in the examples we are considering, a filling system always exists. It is defined by the following diagram:

$$\begin{array}{ccccc}
 & & B_j \times_{\mathfrak{s}[1, jj']} A_{j'} & & \\
 & \swarrow & & \searrow & \\
 B_j & & & & A_{j'} \\
 \downarrow \lambda & & & & \downarrow \lambda' \\
 & & B_j \cup_{\mathfrak{s}[1, jj']} A_{j'} & & \\
 \mathfrak{s}[0, j] & \xrightarrow{\text{inj}_{jj'}} & \mathfrak{s}[1, jj'] & \xleftarrow{\text{inj}_{jj'}} & \mathfrak{s}[0, j']
 \end{array}$$

where $B_j \times_{\mathfrak{s}[1, jj']} A_{j'}$ is a pullback, and where $B_j \cup_{\mathfrak{s}[1, jj']} A_{j'}$ is a pushout above that pullback. Intuitively, it identifies all nodes of B_j and of $A_{j'}$ that have the same underlying state in $\mathfrak{s}[1, jj']$. This is the filling system which will be used in our interpretation of sequential composition. We establish in the Appendix §B that the hypothesis of Proposition 1 holds in the case of the stateful and stateless templates \mathfrak{s}_S and \mathfrak{s}_L . Interestingly, this is not necessarily the case for the template $\mathfrak{s}_{\text{Sep}}$ of separated states regulating the interpretation of CSL proofs. The reason is that there are several ways to decompose a given separated state between two players C, F in $\mathfrak{s}_{\text{Sep}}^{\circ\bullet}$ into a separated state between three players C_1, C_2, F in $\mathfrak{s}_{\text{Sep}}^{\circ\bullet\circ}$.

6 THE ERROR MONAD

We want to keep track of the errors in our transition systems, in particular we want to carefully control how errors propagate in the various constructions. Our method is to use the **error monad** \mathbf{T} over the category of asynchronous graphs, which adds an isolated node \bullet . Then we swap our base category from the category \mathfrak{S} to its category $\mathfrak{S}^{\mathbf{T}}$ of \mathbf{T} -algebras. In the case of asynchronous graphs, a \mathbf{T} -algebra is simply a pointed asynchronous graph (x, G) where x is a node of G , and a morphism

of algebras is an asynchronous morphism which maps the distinguished node of its source to that of its target. In our case, the distinguished node will correspond to the error. As we will see, this category \mathbb{S}^T of T -algebras inherits from \mathbb{S} the properties that are needed to define the operations that we use to interpret programs and proofs. This principled approach to error handling ensures that the error is represented as a single node in the transition systems.

6.1 Liftings

Both the stateful and the stateless machine models have natural T -algebra structures: the distinguished node is the error node \perp . Given an opcategory \mathcal{A} in the category of algebras \mathbb{S}^T , the monad T can be lifted to a monad \tilde{T} on $\mathbf{Cob}(\mathcal{A})$, where we omit the writing of the forgetful functor from the algebra to the underlying category. The idea is to define the image under \tilde{T} of a cobordism to be the cobordism

$$\begin{array}{ccccc}
 TA & \xrightarrow{Tin} & TS & \xleftarrow{Tout} & TB \\
 T\lambda_A \downarrow & & \downarrow T\lambda_\sigma & & \downarrow T\lambda_B \\
 T\mathcal{A}[0] & \xrightarrow{Tin} & T\mathcal{A}[1] & \xleftarrow{Tout} & T\mathcal{A}[0] \\
 a_0 \downarrow & & \downarrow a_1 & & \downarrow a_0 \\
 \mathcal{A}[0] & \xrightarrow{in} & \mathcal{A}[1] & \xleftarrow{out} & \mathcal{A}[0]
 \end{array}$$

where a_0 and a_1 are the structural morphism of $\mathcal{A}[0]$ and $\mathcal{A}[1]$ respectively. The multiplication μ is given for games by the diagram:

$$\begin{array}{ccc}
 TTA & \xrightarrow{\mu} & TA \\
 \downarrow TT\lambda_A & & \downarrow T\lambda_A \\
 TT\mathcal{A}[0] & \xrightarrow{\mu} & T\mathcal{A}[0] \\
 \downarrow T a_0 & & \downarrow a_0 \\
 T\mathcal{A}[0] & \xrightarrow{a_0} & \mathcal{A}[0] \\
 \downarrow a_0 & \nearrow id & \\
 \mathcal{A}[0] & &
 \end{array}$$

and similarly for cobordisms. The case of the unit η follows the same idea. The fact that \tilde{T} is compatible with horizontal composition and that it satisfies the monad laws follows from the fact that the error monad T is a cocartesian monad: as a functor, it preserves pushouts, and all the naturality squares of μ and of η are pushout squares. Further, we have an inclusion from cobordisms in the ambient category of T -algebras into the algebras of the lifted monad \tilde{T} on cobordisms:

$$\mathbf{Cob}_{\mathbb{S}^T}(\mathcal{A}) \hookrightarrow \mathbf{Cob}_{\mathbb{S}}(\mathcal{A})^{\tilde{T}}$$

By monad on a double category \mathcal{D} , we mean a monad in the vertical 2-category \mathbf{DbCat}_v of double categories (see [10]).

In the case of the error monad on asynchronous graphs, the category of T -algebras is complete (as the forgetful functor always creates all limits), cocomplete (as T preserves reflexive coequalizers) and adhesive (as the forgetful functor creates pullbacks and pushouts).

6.2 Tensor product

In order to define the parallel product in this new ambient category, we need a tensor product in the category of T -algebras. We achieve this by lifting the Cartesian product into the **smash product**

of pointed asynchronous graphs $(x_1, G_1) \boxtimes (x_2, G_2)$ which identifies any pair of nodes of the form (x_1, x_2) , $(x_1, -)$ or $(-, x_2)$ and make this node the distinguished node of the smash product. In our case, the smash product is a lifting to the Eilenberg-Moore category of the Cartesian product on asynchronous graphs, in that they commute with both the forgetful functor and the free algebra functors in the expected way. and the forgetful functor in the expected ways. See Lemma 2.20 in [21], and [13] for more details.

7 SEPARATED STATES

We now define the last of the three internal J -opcategories: $\mathfrak{A}_{\text{Sep}}$, that will be used to interpret the proofs of CSL. Its structure is richer than the other two as it is a proper internal J -opcategory, indexed by the set of predicates of CSL. First, we define the notion of separated states, then we define the machine model $\mathfrak{A}_{\text{Sep}}^\bullet$, and finally we define the J -internal category structure $\mathfrak{A}_{\text{Sep}}$.

7.1 Separated states

We use the same notion of separated states as Mellès and Stefanescu [18, 19]. We suppose given an arbitrary partial cancellative commutative monoid **Perm** which we call the **permission monoid**, with a top element \top , following Bornat et al. [3]. We require \top to admit no multiples: $\forall x \in \mathbf{Perm}, \top \cdot x$ is not defined. The set **LState** of logical states is defined in much the same way as the set of memory states, with the addition of permissions to each variable and heap location:

$$(\mathbf{Var} \rightarrow_{fn} (\mathbf{Val} \times \mathbf{Perm})) \times (\mathbf{Loc} \rightarrow_{fn} (\mathbf{Val} \times \mathbf{Perm}))$$

Permissions enable us to define a *separation product* $\sigma * \sigma'$ between two logical states σ and σ' which generalizes disjoint union. When it is defined, the logical state $\sigma * \sigma'$ is defined as a partial function with domain

$$\text{dom}(\sigma * \sigma') = \text{dom}(\sigma) \cup \text{dom}(\sigma')$$

in the following way: for $a \in \mathbf{Var} \amalg \mathbf{Loc}$,

$$\sigma * \sigma'(a) = \begin{cases} \sigma(a) & \text{if } a \in \text{dom}(\sigma) \setminus \text{dom}(\sigma') \\ \sigma'(a) & \text{if } a \in \text{dom}(\sigma') \setminus \text{dom}(\sigma) \\ (v, p \cdot p') & \text{if } \sigma(a) = (v, p) \text{ and } \sigma'(a) = (v, p') \end{cases}$$

The separation product $\sigma * \sigma'$ of the two logical states σ and σ' is not defined otherwise. In particular, the memory states underlying σ and σ' agree on the values of the shared variables and heap locations when the separation product is well defined.

The predicates of CSL are predicates on logical states. Their grammar is the following, it consists mainly in first order logic enriched with the separating conjunction:

$$\begin{aligned} P, Q, R, J ::= & \mathbf{emp} \mid \mathbf{true} \mid \mathbf{false} \mid P \vee Q \mid P \wedge Q \mid \neg P \\ & \mid \forall v. P \mid \exists v. P \mid P * Q \mid v \xrightarrow{L} w \mid \text{own}_p(x) \mid E'_1 = E'_2 \end{aligned}$$

where $x \in \mathbf{Var}$, $p \in \mathbf{Perm}$, $v, w \in \mathbf{Val}$, and the E'_i are arithmetic expressions that can contain metavariables a, b, c, \dots that are used for quantifiers. The semantics of these predicates is given by

the judgment $\sigma \models P$; it is defined by induction on the structure of P , some rules are:

$$\begin{aligned}
(s, h) \models v \overset{p}{\mapsto} w &\iff v \in \mathbf{Loc} \wedge s = \emptyset \wedge h = [v \mapsto (w, p)] \\
(s, h) \models \mathbf{own}_p(x) &\iff \exists v \in \mathbf{Val}, s = [x \mapsto (v, p)] \wedge h = \emptyset \\
\sigma \vdash \mathbf{emp} &\iff \sigma = (\emptyset, \emptyset) \\
\sigma \vdash P * Q &\iff \exists \sigma_1 \sigma_2, \sigma = \sigma_1 * \sigma_2, \sigma_1 \vdash P \wedge \sigma_2 \vdash Q \\
\sigma \vdash P \wedge Q &\iff \sigma \vdash P \text{ and } \sigma \vdash Q \\
\sigma \vdash E_1 = E_2 &\iff \llbracket E_1 \rrbracket = \llbracket E_2 \rrbracket \wedge \mathbf{fv}(E_1 = E_2) \subseteq \mathbf{vdom}(s) \\
\sigma \vdash \exists a. P &\iff \exists x \in \mathbf{Val}, \sigma \vdash P[v/a]
\end{aligned}$$

We recall the notion of *separated state* whose purpose is to separate the logical memory state into one region controlled by the Code, one region controlled by the Frame, and one independent region for each unlocked resource. In order to define the notion, we suppose given a finite set $\mathbb{L} \subseteq \mathbf{Locks}$ of locks, and for each lock r , a predicate I . We write $\Gamma = r_1 : I_1, \dots, r_n : I_n$.

Definition 7.1. A *separated state* is a triple

$$(\sigma_C, \sigma, \sigma_F) \in \mathbf{LState} \times (\mathbb{L} \rightarrow \mathbf{LState} + \{C, F\}) \times \mathbf{LState}$$

such that the logical state below is defined:

$$\sigma_C * \left\{ \bigotimes_{r \in \mathbf{dom}(\sigma)} \sigma(r) \right\} * \sigma_F \in \mathbf{LState} \quad (33)$$

where we write $\mathbf{dom}(\sigma)$ for $\sigma^{-1}(\mathbf{LState})$, and similarly for $\mathbf{dom}_C(\sigma)$ and $\mathbf{dom}_F(\sigma)$, and such that, for all $r_k \in \mathbf{dom}(\sigma)$, $\sigma(r_k) \models I_k$.

The logical state σ_C describes the part of the (logical and refined) memory which is owned by the Code; while σ_F describes the part which is owned by the Frame or the Environment; finally, the function σ indicates, for each lock r , who is holding it between Code and Frame, and if nobody is holding it, which part $\sigma(r)$ of the logical memory the lock is currently protecting, or owning. Note that $\sigma(r)$ is the piece of the logical memory which the Code or the Frame will get when it acquires the lock. For example, in the traditional example where the monoid of permissions is defined as $\mathbf{Perm} = (0, 1]$ with addition, the situation where both the Code and the Environment have read access to a location ℓ can be represented by the separated state $([\ell \mapsto (4, 1/2)], \emptyset, [\ell \mapsto (4, 1/2)])$ where the function σ is empty.

7.2 The machine model of separated states $\mathfrak{A}_{Sep}^\bullet$

In contrast to the stateful and the stateless machine models \mathfrak{A}_S^\bullet and \mathfrak{A}_L^\bullet , which are one-player games, the machine model of separated states involves two players Code and Frame. Similarly to the graphs of states and locks, we define a notion of footprints, which turn out to be the same as the footprints of associated to the machine states:

$$\rho \in \mathcal{P}(\mathbf{Var} + \mathbf{Loc}) \times \mathcal{P}(\mathbf{Var} + \mathbf{Loc}) \times \mathcal{P}(\mathbb{L}) \times \mathcal{P}(\mathbf{Loc}).$$

The **machine model of separated states** $\mathfrak{A}_{Sep}^\bullet[\Gamma]$ is the asynchronous graph whose nodes are the separated states and whose edges are either Code or Frame transitions: Code moves are the

$$(\sigma_C, \sigma, \sigma_F) \xrightarrow{m:C} (\sigma'_C, \sigma', \sigma_F) \quad \text{such that} \quad \bigotimes(\sigma_C, \sigma, \sigma_F) \xrightarrow{m} \bigotimes(\sigma'_C, \sigma', \sigma_F) \quad \text{in } \mathfrak{A}_S^\bullet$$

where $m \in \mathbf{Instr}$ is an instruction, and such that the following conditions are satisfied:

$$\begin{aligned} \forall \ell \notin \text{wr}(m), \sigma_C(\ell) &= \sigma'_C(\ell) & \text{wr}(m) \cup \text{rd}(m) &\subseteq \text{dom}(\sigma_C) \\ \text{lock}(m) &\subseteq \text{dom}(\sigma) \cup \text{dom}_C(\sigma) & \forall r \notin \text{lock}(m), \sigma(r) &= \sigma'(r). \end{aligned}$$

Frame moves are of the form $(\sigma_C, \sigma, \sigma_F) \xrightarrow{m:F} (\sigma_C, \sigma', \sigma'_F)$ with symmetric conditions. In the same way as the other machine models, the tiles of $\mathfrak{A}_{\text{Sep}}^\bullet$ are the squares such that the footprints of the opposite instructions are independent.

7.3 The internal category $\mathfrak{A}_{\text{Sep}}$

Recall that the internal category $\mathfrak{A}_{\text{Sep}}$ is indexed by the set of predicates of CSL. In order to define the asynchronous graph $\mathfrak{A}[0, P]$ for each predicate P , we need to lift the satisfaction relation of a predicate from logical states to separated states by defining:

$$(\sigma_C, \sigma, \sigma_F) \models P \iff \sigma_C \models P.$$

Then, we can define $\mathfrak{A}_{\text{Sep}}[0, P]$ to be the subgraph of $\mathfrak{A}_{\text{Sep}}^\bullet$ induced by the separated states that satisfy P , keeping only the Frame moves. We are now ready to define the internal J -opcategory, the unique support is the whole two player graph $\mathfrak{A}_{\text{Sep}}[1] = \mathfrak{A}_{\text{Sep}}^\bullet$, and the interfaces is the collection of all the $\mathfrak{A}_{\text{Sep}}[0, P]$, with the obvious inclusion morphism into $\mathfrak{A}_{\text{Sep}}[1]$. The map $\mu_P : \mathfrak{A}_{\text{Sep}}[2, P] \rightarrow \mathfrak{A}_{\text{Sep}}[1]$ is given by the universality of the pushout.

7.4 Parallel product understood as a span-monoidal structure on $\mathfrak{A}_{\text{Sep}}$

In order to define the parallel product at the level of proofs, which we will use to interpret the CSL rule for the parallel product, we endow $\mathfrak{A}_{\text{Sep}}$ with a span-monoidal structure. The main piece of this structure is the asynchronous graph $\mathfrak{A}_{\text{Sep}}^{\circ\circ\bullet}$ of *three-player separated states*, which will be the basis for the support of the span

$$\mathfrak{A}_{\text{Sep}} \times \mathfrak{A}_{\text{Sep}} \xleftarrow{\text{pick}} \mathfrak{A}_{\text{Sep}}^{\circ\circ\bullet} \xrightarrow{\text{pince}} \mathfrak{A}_{\text{Sep}}$$

of Definition 4.2. It is the straightforward generalization of separated states to the case where there are three players, C_1, C_2 and F ; its nodes are the states $(\sigma_1, \sigma_2, \sigma, \sigma_F)$, such that the product is well defined. The colors are pairs of predicates, which correspond to σ_1 and σ_2 . The internal functor pince has the following action on the supports of the internal J -categories: it maps a three-player state to the (two-player) separated state $(\sigma_1 * \sigma_2, \sigma', \sigma_F)$, where σ' is the same as σ where C_1 and C_2 have been remapped to C . The morphism pick maps that three-player state to the pair $\langle (\sigma_1, \sigma_1, \sigma_2 * \sigma_F), (\sigma_2, \sigma_1, \sigma_1 * \sigma_F) \rangle$. The unit $\mathfrak{A}_{\text{Sep}}^{\parallel\eta}$ of the structure is the subgraph of $\mathfrak{A}_{\text{Sep}}[1]$ with only Frame moves.

8 CHANGE OF LOCKS

The three machine models $\mathfrak{A} = \mathfrak{A}_{\text{Sep}}, \mathfrak{A}_S, \mathfrak{A}_L$ considered in this paper are parameterized by the set of *free locks* which the programs can access. In the case of the two internal opcategories \mathfrak{A}_S and \mathfrak{A}_L , the free locks are simply described by a set \mathbb{L} of lock names. In the case of the internal opcategories $\mathfrak{A}_{\text{Sep}}$ of separated states, the free locks are described by a context $\Gamma = r_1 : I_1, \dots, r_n : I_n$ which associates to each free lock r_k the predicate I_k of a CSL invariant. As we are considering the general case, we write Γ, r to denote a context in either of the two cases. The operations of *introducing a new lock* and of *creating a critical section* transport cobordisms across machine models parameterized

with different free locks. The change-of-basis operations are induced by the two acute spans below:

$$\begin{array}{ccc} & \text{when}[r] & \\ & \curvearrowright & \\ \mathfrak{z}(\Gamma) & & \mathfrak{z}(\Gamma, r) \\ & \curvearrowleft & \\ & \text{hide}[r] & \end{array}$$

where we write explicitly the dependence of the models $\mathfrak{z}(\Gamma)$ on the contexts (or lists) Γ of free locks. We formalize the situation by defining the graph **LockGraph** whose vertices are the lock contexts Γ , with edges defined as transitions *adding* or *removing* one specific lock in the context:

$$\begin{array}{ccc} & \iota_r^\Gamma & \\ & \curvearrowright & \\ \Gamma & & \Gamma, r \\ & \curvearrowleft & \\ & \pi_r^\Gamma & \end{array}$$

Consider **LockGraph**^{*} the locally posetal bicategory freely generated by this graph, with invertible 2-cells between paths that are equal up to the reorderings: $\pi_r \circ \iota_{r'} \sim \iota_{r'} \circ \pi_r$, $\pi_r \circ \pi_{r'} \sim \pi_{r'} \circ \pi_r$ and $\iota_r \circ \iota_{r'} \sim \iota_{r'} \circ \iota_r$, for $r \neq r'$, and leaving the Γ 's implicit. By locally posetal, we mean that there is at most one tile (or invertible 2-cell) between any pair of morphisms, witnessing that they are equal up to the reorderings above. We call a **LockGraph-template** a pseudo functor from this bicategory to **AcuteSpan**(\mathbb{S}), the bicategory of internal J -opcategories and acute spans we defined in §4.3. The invertible 2-cells in the domain reflect the fact that the order of the operations does not matter, up to isomorphism. The three families $\mathfrak{z} = \mathfrak{z}_{\text{Sep}}, \mathfrak{z}_S, \mathfrak{z}_L$ of internal J -opcategories defined so far are **LockGraph**-templates; we explain how in the remainder of this section. Practically, this means that, by post-composing with the lax functor **Cob**(\cdot), we are able to transport a cobordism defined on the internal category $\mathfrak{z}(\Gamma)$ to one defined on the internal category $\mathfrak{z}(\Gamma, r : I)$, to interpret critical sections, and back, for resource introduction. We abuse the notation and write these lax double functors as follows:

$$\begin{array}{ccc} & \text{when}[r] & \\ & \curvearrowright & \\ \mathbf{Cob}(\mathfrak{z}(\Gamma)) & & \mathbf{Cob}(\mathfrak{z}(\Gamma, r)) \\ & \curvearrowleft & \\ & \text{hide}[r] & \end{array}$$

8.1 Hiding

To hide a lock r , we proceed in two steps for all the templates that we consider $\mathfrak{z} = \mathfrak{z}_L, \mathfrak{z}_S, \mathfrak{z}_{\text{Sep}}$. First, we prevent the Environment from touching that lock, and then we remove this lock from the states and we transform all transitions $P(r), V(r)$ into nops. Formally, hiding is defined by a pull and push operation along the acute span $\text{hide}[r]$:

$$\mathfrak{z}(\Gamma, r) \xleftarrow{\text{inj}_C} \mathfrak{z}^{(r)}(\Gamma, r) \xrightarrow{\text{hide}_C} \mathfrak{z}(\Gamma).$$

The support $\mathfrak{z}^{(r)}(\Gamma, r)$ of the span is defined to be the same as the template $\mathfrak{z}(\Gamma, r)$, except that all Environment transitions $P(r), V(r)$ are deleted, and, only in the case of the template of separated states, we remove the states of the form (σ_C, σ_F) , where the lock r is held by the Environment. By definition of $\mathfrak{z}^{(r)}(\Gamma, r)$ as a restriction of $\mathfrak{z}(\Gamma, r)$, there is a canonical injection inj_C . The map hide_C is defined differently depending on the kind of template we are considering. In the case of the templates used for the code, respectively \mathfrak{z}_L and \mathfrak{z}_S , we map the states $L \subseteq \mathbb{L}$ to $L \setminus \{r\}$, and $s = (\mu, L)$ to $(\mu, L \setminus \{r\})$ respectively. In the case of the template of separated states $\mathfrak{z}_{\text{Sep}}$ used to

interpret proofs, it is defined as follows on separated states:

$$(\sigma_C, \sigma, \sigma_F) \mapsto \begin{cases} (\sigma_C * \sigma(r), \sigma \setminus r, \sigma_F) & \text{if } \sigma(r) \in \mathbf{State} \\ (\sigma_C, \sigma \setminus r, \sigma_F) & \text{if } \sigma(r) = C \end{cases}$$

In all cases, hide_C maps $P(r)$ and $V(r)$ to nop , and otherwise preserves the edges. The intuition behind the definition of hide_C for the separated states is that when we forget about the lock r , even when nobody is holding it, we need to do something with the resources that is associated to the lock: we chose to give this resource to the Code. This means that outside of the binder for r , the resource associated to r is not shared, but belongs to the Code. Remark that the lock r does not appear in the result, which means that the semantics of resource r do C is invariant under α -conversion.

8.2 Critical sections

The case of critical sections is more delicate, as the definition differs between \mathfrak{A}_L , \mathfrak{A}_S on the one hand, and $\mathfrak{A}_{\text{Sep}}$ on the other. In the case of the semantics of the code, we wish to let the Environment be wild and change the states of locks whenever it wants, even if it happens to be held by the Code. On the other hand, when it comes to the semantics of the proofs, we enforce the discipline that a lock can only be unlocked by the player that locked it. This difference of requirements is reflected by differences in the definition of $\text{when}[r]$.

Stateful and stateless semantics. We consider the machine model $\mathfrak{A}_{\langle r \rangle}(\mathbb{L} \uplus \{r\})$ which is the restriction of $\mathfrak{A}(\mathbb{L} \uplus \{r\})$ where *only the Environment* is able to use the lock r . This corresponds to the fact that inside a critical section the Code loses access to the corresponding lock until the end of the section. There exists a map ∇ from $\mathfrak{A}_{\langle r \rangle}(\mathbb{L} \uplus \{r\})$ to $\mathfrak{A}(\mathbb{L} \uplus \{r\})$ given by:

$$\begin{aligned} (\mu, L) &\mapsto (\mu, L \cap \mathbb{L}) \\ P(r), V(r) : F &\mapsto \text{nop} \\ m &\mapsto m \end{aligned}$$

Then, the lifting of a cobordism for the critical sections is given by the following acute span:

$$\mathfrak{A}(\mathbb{L}) \xleftarrow{\nabla} \mathfrak{A}_{\langle r \rangle}(\mathbb{L} \uplus \{r\}) \xrightarrow{\text{incl}} \mathfrak{A}(\mathbb{L} \uplus \{r\})$$

where the right leg is the obvious inclusion. Intuitively, the operation of pulling along ∇ , which defines the $\text{when}[r]$ operation, duplicates the whole transition system, with one version for each state of the lock r . It is the Environment which is in control of this, and the Code is oblivious to the state of the lock r .

Separated state semantics. The lifting operation, that we will use to deal with critical sections, is simply defined as the push-forward along the map

$$\mathfrak{A}_{\text{Sep}}(\Gamma) \longrightarrow \mathfrak{A}_{\text{Sep}}(\Gamma, r : I)$$

which sends a separated state $(\sigma_C, \sigma, \sigma_F)$ over Γ to the separated state over $\Gamma, r : I$ where the lock is held by the code $(\sigma_C, \sigma \uplus [r \mapsto C], \sigma_F)$.

9 A UNIFORM INTERPRETATION OF CODES AND PROOFS

We have carefully studied in previous sections how to express the main operations of concurrent separation logic (sequential composition, parallel product and change of lock) in the conceptual language of template games and cobordisms inspired from [16]. Now that each of these basic

operations has been rigorously defined, the interpretation of the code and of the proofs of concurrent separation logic (CSL) is uniform and essentially straightforward.

9.1 Stateful and stateless interpretations of the code

We begin by describing how the stateful and stateless interpretations $\llbracket C \rrbracket_S$ and $\llbracket C \rrbracket_L$ of a given code C are computed in our template game model. Since the interpretation is uniform in \mathfrak{z}_S and \mathfrak{z}_L and does only depend on the combinators of C , we find convenient to write \mathfrak{z} alternatively for \mathfrak{z}_S or \mathfrak{z}_L . We recall below the grammar of the concurrent programming language with shared memory, dynamic allocation and locks designed by Brookes [4] in his seminal paper on the semantics of CSL:

$$\begin{aligned} B &::= \mathbf{true} \mid \mathbf{false} \mid B \wedge B' \mid B \vee B' \mid E = E' & E &::= 0 \mid 1 \mid \dots \mid x \mid E + E' \mid E * E' \\ C &::= x := E \mid x := [E] \mid [E] := E' \mid \mathbf{skip} \mid \mathbf{dispose}(E) \mid x := \mathbf{malloc}(E) \mid C; C' \mid C_1 \parallel C_2 \\ &\mid \mathbf{while} B \mathbf{do} C \mid \mathbf{resource} r \mathbf{do} C \mid \mathbf{with} r \mathbf{do} C \mid \mathbf{if} B \mathbf{then} C_1 \mathbf{else} C_2 \end{aligned}$$

The code C is interpreted by structural induction as a (pointed) cobordism of the form

$$\llbracket C \rrbracket = \begin{array}{ccccc} \llbracket C \rrbracket_{in} & \longrightarrow & \llbracket C \rrbracket_{support} & \longleftarrow & \llbracket C \rrbracket_{out} \\ \lambda_{in} \downarrow & & \downarrow & & \downarrow \lambda_{out} \\ \mathfrak{z}(\mathbb{L})[0] & \longrightarrow & \mathfrak{z}(\mathbb{L})[1] & \longleftarrow & \mathfrak{z}(\mathbb{L})[0]. \end{array} \quad (34)$$

where the set L of available locks is taken as implicit parameter. The interpretation of every non-leaf command of the language corresponds to an operation on cobordisms already defined, and it is thus straightforward. Then, except for \mathbf{malloc} which is treated in full details in the Appendix, every leaf command $x := E \mid x := [E] \mid [E] := E' \mid \mathbf{skip} \mid \mathbf{dispose}(E)$ of the language corresponds to a specific machine instruction m which is interpreted as a cobordism $\llbracket m \rrbracket$ living in the double category $\mathbf{Cob}(\mathfrak{z}(\mathbb{L}))$, with the set \mathbb{L} of available locks as implicit parameter. The cobordism $\llbracket m \rrbracket$ is constructed in the following way: its input and output borders $\llbracket m \rrbracket_{in}$ and $\llbracket m \rrbracket_{out}$ are defined as the asynchronous graph $\mathfrak{z}(\mathbb{L})[0]$, while its support $\llbracket m \rrbracket_{support}$ consists of the disjoint union of $\llbracket m \rrbracket_{in}$ and $T\llbracket m \rrbracket_{out}$ augmented with an edge $s_1 \rightarrow s_2$ from $s_1 \in \llbracket m \rrbracket_{in}$ to $s_2 \in T\llbracket m \rrbracket_{out}$ for every machine transition $m : s_1 \rightarrow s_2$ performed by the instruction m . Note that $\llbracket m \rrbracket_{in}$ and $\llbracket m \rrbracket_{out}$ contain only Frame transitions, and that all the “transverse” edges $s_1 \rightarrow s_2$ from $\llbracket m \rrbracket_{in}$ to $T\llbracket m \rrbracket_{out}$ are Code transitions, with the state s_2 potentially equal to the error state.

We now detail how to give a semantics to any code C as a cobordism $\llbracket C \rrbracket$, by induction on its structure. This lets us build $\llbracket C \rrbracket_S$ and $\llbracket C \rrbracket_L$ in the same way.

Instructions. We explain first how to define the cobordism that interprets a single instruction m using a well chosen pullback. Consider the following asynchronous graph

$$A = \begin{array}{ccc} F_1 & & F_2 \\ \curvearrowright & & \curvearrowright \\ \bullet & \xrightarrow{C} & \bullet \end{array}$$

with a tile $F_1 \cdot C \sim C \cdot F_2$. Then, we can construct the pullback, where \mathbf{Instr} is the set of instruction:

$$\begin{array}{ccc} G(m) & \longleftarrow & A \times \mathfrak{z}^1 \\ \downarrow & & \downarrow f \\ \Omega(\{F, m\}) & \longleftarrow & \Omega(\{F\} \cup \mathbf{Instr}) \end{array}$$

where the map f sends edges of the form (F_1, \cdot) and $((F_2, \cdot))$ to F and edges of the form (C, m') to the edge m' in $\Omega(\{F\} \cup \mathbf{Instr})$, for all instructions m' . It is then easy to deduce the maps from the borders using the universal property of the pullback.

Leaf codes. For leaf codes that correspond to instructions (all, except for `malloc`), their semantics is defined to be the same as that of the corresponding instruction. For `malloc(E)`, we take the disjoint sum of all the `alloc(E, ℓ)`:

$$\llbracket \text{malloc}(E) \rrbracket := \bigoplus_{\ell \in \text{Loc}} \llbracket \text{alloc}(E, \ell) \rrbracket$$

Conditionals. Conditional branching is interpreted as

$$\llbracket \text{if } B \text{ then } C_1 \text{ else } C_2 \rrbracket$$

defined as

$$\llbracket \text{test}(B) \rrbracket; \llbracket C_1 \rrbracket \oplus \llbracket \text{test}(\neg B) \rrbracket; \llbracket C_2 \rrbracket \quad (35)$$

recomposed with

$$\text{fill}(\pm[0] \xrightarrow{id} \pm[0], \pm[0] + \pm[0] \xrightarrow{\nabla} \pm[0])$$

Here, the purpose of precomposing with the filling is to identify the two copies of the input $\pm[0]$ appearing on each sides of the disjoint sum (35).

Sequential and parallel compositions. We use the sequential and the parallel product of cobordisms to interpret their syntactic counterparts:

$$\llbracket C_1 \parallel C_2 \rrbracket = \llbracket C_1 \rrbracket \parallel \llbracket C_2 \rrbracket \quad \llbracket C_1; C_2 \rrbracket = \llbracket C_1 \rrbracket \mathbin{\text{;}} \llbracket C_2 \rrbracket.$$

Resource introduction and critical sections. We use the change of locks operations. The interpretation of resource r do C is defined as

$$\llbracket \text{resource } r \text{ do } C \rrbracket = \text{hide}[r](\llbracket C \rrbracket)$$

The interpretation of $\llbracket \text{with } r \text{ do } C \rrbracket$ is defined as:

$$\llbracket P(r) \rrbracket \mathbin{\text{;}} \text{when}[r](\llbracket C \rrbracket) \mathbin{\text{;}} \llbracket V(r) \rrbracket.$$

Loops. For loops, the interpretation of $C' = \text{while } B \text{ do } C$ is defined as the unfolding of

$$X \mapsto \llbracket \text{test}(B) \rrbracket \mathbin{\text{;}} \llbracket C \rrbracket \oplus \llbracket \text{test}(\neg B) \rrbracket$$

see the Appendix for details.

9.2 Interpretation of the proofs

Machine instructions. The rules that correspond to machine instructions $m \in \mathbf{Instr}$ (such as `LOAD`) are interpreted in the obvious way, always preserving the permission associated to affected locations.

9.3 Interactive and separated interpretations of the proofs

We recall in Fig. 1 the main inference rules of concurrent separation logic (CSL) as it appears in its original form, see Brookes [4]. The inference rule `RES` associated to resource r do C moves to the shared context Γ a piece of the logical state owned and potentially used by the Code, so that the new resource $r : J$ can be accessed concurrently inside the code C . However, the access to that resource $r : J$ is typically mediated by the `with` constructor, which grants temporary access under the condition that one gives it back. Note that the rule `WITH` has the side condition $P \Rightarrow \text{def}(B)$ which means that if P is true in some logical state, then it implies that for every free variable x of B , there exists a permission p such that $\text{own}_p(x)$ holds. At this stage, our purpose is to interpret by

$$\begin{array}{c}
 \frac{\Gamma \vdash \{P\}C_1\{Q\} \quad \Gamma \vdash \{Q\}C_2\{R\}}{\Gamma \vdash \{P\}C_1; C_2\{R\}} \text{SEQ} \qquad \frac{\Gamma \vdash \{P_1\}C\{Q_1\} \quad \Gamma \vdash \{P_2\}C\{Q_2\}}{\Gamma \vdash \{P_1 \vee P_2\}C\{Q_1 \vee Q_2\}} \text{DISJ} \qquad \frac{\Gamma, r : J \vdash \{P\}C\{Q\}}{\Gamma \vdash \{P * J\}\text{resource } r \text{ do } C\{Q * J\}} \text{RES} \\
 \\
 \frac{P \Rightarrow \text{def}(B) \quad \Gamma \vdash \{(P * J) \wedge B\}C\{Q * J\}}{\Gamma, r : J \vdash \{P\}\text{with } r \text{ do } C\{Q\}} \text{WITH} \qquad \frac{\Gamma \vdash \{P_1\}C_1\{Q_1\} \quad \Gamma \vdash \{P_2\}C_2\{Q_2\}}{\Gamma \vdash \{P_1 * P_2\}C_1 \parallel C_2\{Q_1 * Q_2\}} \text{PAR} \qquad \frac{\Gamma \vdash \{P\}C\{Q\}}{\Gamma \vdash \{P * R\}C\{Q * R\}} \text{FRAME}
 \end{array}$$

Fig. 1. Inference rules of Concurrent Separation Logic

structural induction every CSL proof $\pi : \Gamma \vdash \{P\}C\{Q\}$ as a cobordism

$$\begin{array}{ccc}
 \llbracket \pi \rrbracket_{\text{Sep}, \text{in}} & \longrightarrow & \llbracket \pi \rrbracket_{\text{Sep}, \text{support}} \longleftarrow \llbracket \pi \rrbracket_{\text{Sep}, \text{out}} \\
 \downarrow & & \downarrow \qquad \qquad \downarrow \\
 \mathfrak{A}_{\text{Sep}}(\Gamma)[0, P] & \longrightarrow & \mathfrak{A}_{\text{Sep}}(\Gamma)[1] \longleftarrow \mathfrak{A}_{\text{Sep}}(\Gamma)[0, Q].
 \end{array} \tag{36}$$

living in the double category $\mathbf{Cob}(\mathfrak{A}_{\text{Sep}}(\Gamma))$ associated to the template $\mathfrak{A}_{\text{Sep}}(\Gamma)$ of separated states, parameterized by the context Γ . As it stands, the interpretation is essentially straightforward, since most of the rules of the logic correspond to an operation on cobordisms already carefully defined. We refer the reader to the Appendix for the comprehensive definitions. There is apparently one exception however: the FRAME rule does not seem, at least at first sight, to correspond to a basic operation on cobordisms. Given a cobordism $\llbracket \pi \rrbracket_{\text{Sep}}$ which interprets a proof π of the Hoare triple $\Gamma \vdash \{P\}C\{Q\}$, we need to define a new cobordism associated to the Hoare triple $\Gamma \vdash \{P * R\}C\{Q * R\}$. The solution is not difficult to find however: we define the new cobordism as the parallel product $\llbracket \pi \rrbracket_{\text{Sep}} \parallel \mathfrak{A}_{\text{Sep}}[0, R]$, where the asynchronous graph $\mathfrak{A}_{\text{Sep}}[0, R]$ is lifted to the identity cobordism defined in the expected way in the double category $\mathbf{Cob}(\mathfrak{A}_{\text{Sep}})$.

$$\left\| \frac{\begin{array}{c} \vdots \pi_1 \\ \Gamma \vdash \{P\}C_1\{Q\} \quad \Gamma \vdash \{Q\}C_2\{R\} \\ \vdots \pi_2 \end{array}}{\Gamma \vdash \{P\}C_1; C_2\{R\}} \right\|_{\text{Sep}} = \llbracket \pi_1 \rrbracket_{\text{Sep}} \mathfrak{A}_{\text{Sep}} \llbracket \pi_2 \rrbracket_{\text{Sep}}$$

For the parallel product rule PAR, we use the parallel product of ATSSs using the above notion of *compatibility*:

$$\begin{aligned}
& \left[\frac{\begin{array}{c} \vdots \pi_1 \\ \Gamma \vdash \{P_1\} C_1 \{Q_1\} \end{array} \quad \begin{array}{c} \vdots \pi_2 \\ \Gamma \vdash \{P_2\} C_2 \{Q_2\} \end{array}}{\Gamma \vdash \{P_1 * P_2\} C_1 \parallel C_2 \{Q_1 * Q_2\}} \right]_{Sep} &= \llbracket \pi_1 \rrbracket_{Sep} \parallel \llbracket \pi_2 \rrbracket_{Sep} \\
& \left[\frac{\begin{array}{c} \vdots \pi \\ \Gamma \vdash \{P\} C \{Q\} \end{array}}{\Gamma \vdash \{P * R\} C \{Q * R\}} \right]_{Sep} &= \llbracket \pi \rrbracket_{Sep} \parallel id_{\mathfrak{A}_{Sep}[0, R]} \\
& \left[\frac{\begin{array}{c} \vdots \pi \\ \Gamma, r : J \vdash \{P\} C \{Q\} \end{array}}{\Gamma \vdash \{P * J\} \text{resource } r \text{ do } C \{Q * J\}} \right]_{Sep} &= \text{hide}[r](\llbracket \pi \rrbracket) \\
& \left[\frac{\begin{array}{c} \vdots \pi \\ \Gamma, r : J \vdash \{(P * J) \wedge B\} C \{Q * J\} \end{array}}{\Gamma, r : J \vdash \{P\} \text{with } r \text{ do } C \{Q\}} \right]_{Sep} &= (\text{acquire}[r]) ; \text{when}[r](\llbracket \pi \rrbracket_{Sep}) ; \text{release}[r] \\
& \left[\frac{\begin{array}{c} \vdots \pi_1 \\ \Gamma \vdash \{P_1\} C \{Q_1\} \end{array} \quad \begin{array}{c} \vdots \pi_2 \\ \Gamma \vdash \{P_2\} C \{Q_2\} \end{array}}{\Gamma \vdash \{P_1 \vee P_2\} C \{Q_1 \vee Q_2\}} \right]_{Sep} &= \llbracket \pi_1 \rrbracket_{Sep} \cup \llbracket \pi_2 \rrbracket_{Sep}
\end{aligned}$$

10 THE ASYNCHRONOUS SOUNDNESS THEOREM REVISITED

We briefly explain how our principled and axiomatic description of CSL leads us to a radically new way to understand and also to prove the asynchronous soundness theorem recently established by Mellès and Stefanescu [19].

10.1 Comparing the three interpretations

We have just shown how to recover by purely conceptual means the three interpretations $\llbracket - \rrbracket_S$, $\llbracket - \rrbracket_L$ and $\llbracket - \rrbracket_{Sep}$ of the codes and proofs of CSL designed by Mellès and Stefanescu in their proof of the asynchronous soundness theorem. Given a code C and a proof π of $\Gamma \vdash \{P\} C \{Q\}$, their proof of asynchronous soundness relies on the existence of a chain of *translations*

$$\llbracket \pi \rrbracket_{Sep} \xrightarrow{\mathcal{S}} \llbracket C \rrbracket_S \xrightarrow{\mathcal{L}} \llbracket C \rrbracket_L \quad (37)$$

between cobordisms living respectively in $\mathfrak{A}_{Sep}(\Gamma)$, $\mathfrak{A}_S(\mathbb{L})$ and $\mathfrak{A}_L(\mathbb{L})$. Here, we write \mathbb{L} for the domain of Γ . In order to clarify the functorial nature of these translations, one starts by observing the existence of internal functors between the colored internal opcategories:

$$\mathfrak{A}_{Sep}(\Gamma) \xrightarrow{u_{\mathcal{S}}} \mathfrak{A}_S(\mathbb{L}) \xrightarrow{u_{\mathcal{L}}} \mathfrak{A}_L(\mathbb{L}).$$

The first internal functor $u_{\mathcal{S}}$ transports every separated state $(\sigma_C, \sigma, \sigma_F)$ into the machine state obtained by multiplying all its components as in (33) and by forgetting all the permissions. The second internal functor $u_{\mathcal{L}}$ forgets the memory from a machine state in order to obtain the corresponding lock state. The three systems of tiles which equip the synchronization templates \mathfrak{A}_{Sep} , \mathfrak{A}_S and \mathfrak{A}_L were carefully designed in order to ensure that these internal functors do indeed exist. Since we can compare by a simulation two cobordisms defined over the same internal J -opcategory, we can also compare by “change-of-basis” two cobordisms over different internal

J -opcategories. As a matter of fact, the translations in (37) are simulations:

$$\text{push}[u_{\mathcal{S}}](\llbracket \pi \rrbracket_{\text{Sep}}) \xrightarrow{\mathcal{S}} \llbracket C \rrbracket_S \quad \text{and} \quad \text{push}[u_{\mathcal{L}}](\llbracket C \rrbracket_S) \xrightarrow{\mathcal{L}} \llbracket C \rrbracket_L.$$

The definition of internal J -functor implies that we have a natural family of isomorphisms

$$\text{push}[u](C; D) \xrightarrow{\text{iso}} \text{push}[u](C); \text{push}[u](D)$$

in the ambient category $\mathbb{S} = \mathbf{AsynGph}$. Intuitively, the reason why this is an isomorphism is that, in both cases, we perform the same pushout for cospan composition $C, D \mapsto C; D$ at the level of the supports of games and cobordisms. The corresponding (non invertible) comparison map associated to the parallel product

$$\text{push}[u](C \parallel D) \longrightarrow \text{push}[u](C) \parallel \text{push}[u](D)$$

is derived from the fact that the internal functor u can be equipped with the structure of a span-monoidal functor, whose main component is a map u^{\parallel} that makes the following diagram commute:

$$\begin{array}{ccccc} \mathfrak{z} \times \mathfrak{z} & \xleftarrow{\text{pick}} & \mathfrak{z}^{\parallel} & \xrightarrow{\text{pince}} & \mathfrak{z} \\ u \times u \downarrow & & \downarrow u^{\parallel} & & \downarrow u \\ \mathfrak{z}' \times \mathfrak{z}' & \xleftarrow{\text{pick}} & \mathfrak{z}'^{\parallel} & \xrightarrow{\text{pince}} & \mathfrak{z}' \end{array}$$

10.2 The asynchronous soundness theorem

Suppose given two cobordisms σ and τ with respective supports S and T on one of the three machine models $\mathfrak{z} = \mathfrak{z}_{\text{Sep}}, \mathfrak{z}_S, \mathfrak{z}_L$, together with an asynchronous morphism $f : S \rightarrow T$ satisfying the expected equation $\lambda_{\sigma} = \lambda_{\tau} \circ f$. Following the terminology introduced by Melliès and Stefanescu [19], we declare that the morphism $f : A \rightarrow B$ is

- a **Code 1-fibration** when every transition $n : f(s) \rightarrow s'$ performed by the Code in B can be lifted to a transition $m : s \rightarrow s''$ performed by the Code in A , such that $f(m) = n$,
- a **2-fibration** when every tile of the form $f(s_1 \rightarrow s_2 \rightarrow s_3) \sim f(s_1) \rightarrow t'_2 \rightarrow f(s_3)$ in B can be lifted to a tile $s_1 \rightarrow s_2 \rightarrow s_3 \sim s_1 \rightarrow s'_2 \rightarrow s_3$ in A , satisfying in particular $f(s'_2) = t'_2$.

We can now state the asynchronous soundness theorem for CSL established by Melliès and Stefanescu [19] which relies on these two notions of fibrations in order to express the *safety* and *data-race freedom* of the code in a clean topological way. In order to prove these two properties, the theorem focuses on the nature of the asynchronous comparison maps between cobordisms

$$\llbracket \pi \rrbracket_{\text{Sep}} \xrightarrow{\mathcal{S}} \llbracket C \rrbracket_S \xrightarrow{\mathcal{L}} \llbracket C \rrbracket_L$$

discussed in §10.1. The theorem states that for every CSL proof π of $\Gamma \vdash \{P\}C\{Q\}$,

THEOREM 10.1 (ASYNCHRONOUS SOUNDNESS THEOREM). *The comparison map $\mathcal{S} : \llbracket \pi \rrbracket_{\text{Sep}} \rightarrow \llbracket C \rrbracket_S$ is a Code 1-fibration, and the comparison map $\mathcal{L} \circ \mathcal{S} : \llbracket \pi \rrbracket_{\text{Sep}} \rightarrow \llbracket C \rrbracket_L$ is a 2-fibration.*

The first part of the theorem ensures that a program specified in CSL does not crash. Indeed, the cobordism $\llbracket \pi \rrbracket_{\text{Sep}}$ lives above $\mathfrak{z}_{\text{Sep}}[1]$ which does not include the error state \perp . Since every step performed by the Code in $\llbracket C \rrbracket_S$ can be lifted to $\llbracket \pi \rrbracket_{\text{Sep}}$ by the 1-fibrational property, the specified Code cannot produce any error. The second part of the theorem ensures that a specified program does not produce nor encounter any data race. Indeed, every time two instructions are executed in parallel in the machine, they define a tile in the cobordism $\llbracket C \rrbracket_L$, which can be lifted by the 2-fibrational property to a tile in the cobordism $\llbracket \pi \rrbracket_{\text{Sep}}$ of separated states. There, the very existence of the tile implies that these two instructions are independent and do not produce any data race.

The conceptual and axiomatic framework of template games enables us to reunderstand in a radically new way the original proof of the asynchronous soundness theorem [19] by reducing it to the preservation properties of the 1-dimensional and 2-dimensional fibrations with respect to pullbacks, in the hom-categories of cobordisms. The proof relies on the observation that the chain of translations (37) induces a sequence of pullback diagrams between the interpretation of the code in (34) and of the CSL proof in (36):

$$\begin{array}{ccccc}
 \llbracket \pi \rrbracket_{Sep,in} & \longrightarrow & \llbracket \pi \rrbracket_{Sep,support} & \longleftarrow & \llbracket \pi \rrbracket_{Sep,out} \\
 \downarrow & \text{pullback} & \downarrow & \text{pullback} & \downarrow \\
 \llbracket C \rrbracket_{S,in} & \longrightarrow & \llbracket C \rrbracket_{S,support} & \longleftarrow & T\llbracket C \rrbracket_{S,out} \\
 \downarrow & \text{pullback} & \downarrow & \text{pullback} & \downarrow \\
 \llbracket C \rrbracket_{L,in} & \longrightarrow & \llbracket C \rrbracket_{L,support} & \longleftarrow & T\llbracket C \rrbracket_{L,out}
 \end{array}$$

These pullback diagrams indicate that the input and output of the various cobordisms interpreting C and π behave properly from the point of view of the translations. This pullback property is a remarkable consequence of the fact that the ambient category $\mathbf{AsynGph}$ of asynchronous graphs is adhesive, as a presheaf category, see E.1.1 for a definition of adhesivity. Then, building on the existence of these pullback diagrams, one establishes using careful and diagrammatic arguments around the notion of van Kampen square (see Lemma 5) a key compatibility property between the 2-dimensional fibrations (stable by limits) and the pushout (a colimit) defining the sequential composition.

10.3 Structural properties of cobordisms

The proof of Theorem 10.1 relies on the cobordisms that interpret programs and on proofs to have strong structural properties. Most of these properties are of a fibrational nature, ranging in a wider gamut than what we have introduced so far. In order to handle uniformly this variety of notions of fibrations, it is quite useful to express them as right-lifting properties. Given an asynchronous morphism $f : G \rightarrow H$ between two asynchronous graphs whose edges have C and F polarities, we define the notions of **Code 1-fibration**, **Environment 1-fibration**, **Environment 1-op-fibration** and of **2-fibration** as right lifting properties, denoted in the four diagrams below, respectively. Each means that whenever there is such a square that commute, there exists a map as the one denoted by a dashed arrow that makes the two triangle commute.

$$\begin{array}{cccc}
 \begin{array}{ccc} \bullet & \longrightarrow & G \\ s \downarrow & \nearrow & \downarrow f \\ \bullet \rightarrow_{C^*} & \longrightarrow & H \end{array} &
 \begin{array}{ccc} \bullet & \longrightarrow & G \\ s \downarrow & \nearrow & \downarrow f \\ \bullet \rightarrow_{F^*} & \longrightarrow & H \end{array} &
 \begin{array}{ccc} \bullet & \longrightarrow & G \\ t \downarrow & \nearrow & \downarrow f \\ \bullet \rightarrow_{F^*} & \longrightarrow & H \end{array} &
 \begin{array}{ccc} \wedge & \longrightarrow & G \\ u \downarrow & \nearrow & \downarrow f \\ \blacklozenge & \longrightarrow & H \end{array}
 \end{array}$$

The asynchronous graph \bullet denotes the graph consisting of a single node; $\bullet \rightarrow_{C^*}$ denotes the graph made up of two nodes and a single Code arrow between them, $\bullet \rightarrow_{F^*}$ denotes the same, but with a Frame polarity; \wedge denotes the asynchronous graph made up of a path of length 2, and finally \blacklozenge is the graph made up of a single tile. The two maps called s map the node into the source of the edge, t maps into the target, and u maps the path of length 2 into the upper path of the tile. A map that is both a fibration and an opfibration is called a **bifibration**. We say that a **map of cobordism is a fibration** of a certain kind when its underlying map between the supports of the cobordisms is a

fibration of that kind. One advantage of this formulation by right-lifting is that it makes is obvious that all notions of fibration are *stable under pullback*, which will be extremely useful in the sequel.

We now state the structural properties that the cobordisms that correspond to interpretations of proofs and of programs satisfy. With the following notations for the cobordism,

$$\begin{array}{ccccc}
 A & \xrightarrow{\text{in}} & S & \xleftarrow{\text{out}} & B \\
 \lambda_A \downarrow & & \downarrow \lambda_\sigma & & \downarrow \lambda_B \\
 \mathfrak{z}[0, i] & \xrightarrow{\text{in}_{ij}} & \mathfrak{z}[1, ij] & \xleftarrow{\text{out}_{ij}} & \mathfrak{z}[0, j]
 \end{array}$$

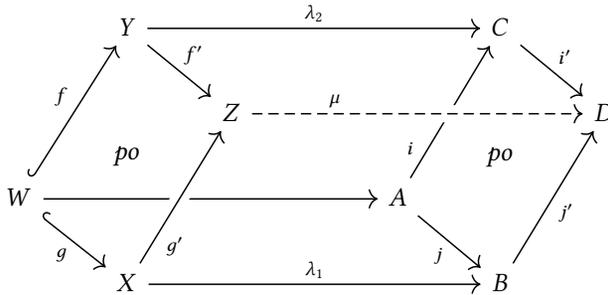
- (1) the map $\text{in} : A \rightarrow \mathfrak{z}[0, i]$ is an epi,
- (2) the map $\lambda_\sigma : A \rightarrow \mathfrak{z}[1, ij]$ is an Environment 1-fibration,
- (3) the map $\text{out} : B \rightarrow S$ is a monomorphism,
- (4) the pullback of in along out is the initial object.

Conditions (1) and (2) are related to receptivity in game semantics: the program must let the environment start from any state, and it must accept any legal mode that the environment wishes to play. The last two conditions are more technical and are needed to ensure that sequential composition behaves nicely. Moreover, we ask of each of our templates that the maps in_{ij} and anchorout_{ij} be monos, Environment 1-fibrations and 2-fibrations

We prove that all interpretations of proofs and of programs satisfy these conditions by induction on their structures. The case of the parallel product is quite simple. Conditions (1) and (4) are preserved because epis and monos are stable under pullbacks (all epis are regular in **AsynGph**), and $\text{pince} : \mathfrak{z}_{\text{Sep}}^\otimes[P] \rightarrow \mathfrak{z}_{\text{Sep}}[P]$ is an epimorphism. Conditions (2) and (3) are preserved because fibrations are preserved by products and pullbacks.

The case of sequential composition is bit more interesting. Condition (1) is obvious, condition (4) follows from the fact that monos are preserved under pushouts in an adhesive category. Preservation of condition (2) is less obvious, and relies on the fact that the walking node and the walking arrow $\cdot \rightarrow \cdot$ is **tiny**, (which means that the functor $\mathbf{Hom}(\cdot \rightarrow \cdot, -)$ preserves small colimits) since they are representable. It follows from the following lemma:

LEMMA 2. *Given the following diagram:*



where λ_1, λ_2, i and j are Environment 1-fibrations, and i, i', j, j' are monos. Then the map μ induced by the universality of the pushout is an Environment 1-fibration as well.

PROOF. Suppose we have a node x in Z which is mapped to the source node of an Environment transition t in D . Because the walking arrow is tiny, either B or C contains a transition t' which is mapped to the transition t . Without loss of generality, suppose that it is in B . Similarly, the node x has an antecedent x' in either X or Y . Suppose first that it is in X . Then, because j' is a monomorphism, it must be that this node is mapped by λ_1 to the source node of t' , and then we conclude using the fact that λ_1 is an Environment 1-fibration. Suppose now that this node x' is in

Y. Then the node $\lambda_2(x')$ in C is mapped to the same node in D as the source node of t' , therefore there is a node y in A which is mapped under j to the source node of t' , and under i to $\lambda_2(x')$. We get back to the previous case by using the fact that j is an Environment 1-fibration. \square

We now establish that composing with a filling system also preserves these structural properties. This is where condition (4) comes into play. The reason condition (3), which states that the map from the output states to the support is a mono, is preserved is that the inputs states I and the output states O are disjoint, which implies that the possible identifications the input states that happens during the pushout do not interfere with the output states.

LEMMA 3. *Given the following commutative diagram, where the square is a pushout and o' is a mono:*

$$\begin{array}{ccccc}
 & & M & & O' \\
 & \swarrow o & & \searrow i' & \swarrow o' \\
 C & & & & C' \\
 & \searrow f & & \swarrow g & \\
 & & D & &
 \end{array}$$

if the pullback of i' and o' is the initial object, then the composite $o' \circ g$ is a mono. This clearly implies that composing with a filling system preserves condition (3).

PROOF. First, we establish that the C' contains the disjoint union of the image of M under i' and the image of O' under o' . For that purpose, consider an epi-mono factorization $i'_2 \circ i'_1$ of i' and the following diagram, where the two squares are pullbacks:

$$\begin{array}{ccc}
 M & \longleftarrow & \emptyset \\
 i'_1 \downarrow & & \downarrow \\
 C'_M & \longleftarrow & X \\
 i'_2 \downarrow & & \downarrow \\
 C' & \longleftarrow & O'
 \end{array}$$

As epimorphisms are preserved under pullbacks in **AsynGph**, the map $\emptyset \rightarrow X$ is an epi, which means that X is the initial object \emptyset . From that, we deduce that the map

$$I' + O' \longrightarrow C'_M$$

is a monomorphism, using the fact that colimits are universal. This is an instance of the general fact that the union of two subobjects is given by the pushout above the pullback. Now, consider the

following diagram, where all squares are pushouts:

$$\begin{array}{ccccc}
 C & \xleftarrow{o} & M & & \\
 \downarrow & & \downarrow i'_1 & & \\
 A & \xleftarrow{h} & C'_M & & \\
 \downarrow & & \downarrow \iota_1 & & \\
 A + O' & \xleftarrow{h+id} & C'_M + O' & \xleftarrow{\iota_2} & O' \\
 \downarrow k & & \downarrow [i'_2, o'] & & \\
 D & \xleftarrow{g} & C' & &
 \end{array}$$

Because monos are preserved under pushouts, the composite $k \circ (h + id) \circ \iota_2 = k_2 = o'$ is indeed a mono, where we write $k = [k_1, k_2]$. \square

10.4 Structural properties of the comparison maps

As we explained above, the comparisons maps

$$\llbracket \pi \rrbracket_{Sep} \xrightarrow{\mathcal{S}} \llbracket C \rrbracket_S \xrightarrow{\mathcal{L}} \llbracket C \rrbracket_L$$

of the Theorem satisfy a strong structural property which we call **strictness**.

Definition 10.2. Suppose given two cobordisms $C \in \mathbf{Cob}(\pm)$ and $B \in \mathbf{Cob}(\pm')$, we say that a map of cobordisms $(f, f_{\pm}) : A \rightarrow B$ is **strict** when the two squares in the following diagram are pullbacks.

$$\begin{array}{ccccc}
 I & \xrightarrow{in} & C & \xleftarrow{out} & O \\
 f_I \downarrow & & \downarrow f_C & & \downarrow f_O \\
 I' & \xrightarrow{in'} & C' & \xleftarrow{out'} & O'
 \end{array}$$

Again, we prove this fact by induction on the structure of the proof and of the programs. The fact that parallel product preserves strictness follows from the fact that it is defined using pullbacks, and from the usual pullback pasting lemma; see Lemma 7 in the Appendix for a detailed proof.

The case of the sequential composition is quite interesting, as it follows from the fact that the ambient category is adhesive. The diagrammatic situation is the following:

$$\begin{array}{ccccccc}
 I & & & M & & & O \\
 \downarrow & \searrow & & \downarrow & \searrow & & \downarrow \\
 I' & & C_1 & & M' & & C_2 & & O' \\
 & \searrow & \downarrow & \searrow & \downarrow & \searrow & \downarrow & \searrow & \\
 & & C'_1 & & C_1; C_2 & & C'_2 & & \\
 & & \downarrow & & \downarrow & & \downarrow & & \\
 & & & & C'_1; C'_2 & & & &
 \end{array}
 \tag{38}$$

Focusing on the commutative cube, what we need to show is that the two front faces are pullbacks. This follows from the fact that the bottom square is a van Kampen square, since it is a pushout along a monomorphism in an adhesive category. Given that the top face is a pushout as well, the definition of van Kampen square gives us directly that the two front faces are pullbacks if and only if the two back faces are pullbacks, which they are by hypothesis.

10.5 Proof of the Theorem

Equipped with the structural properties of the two sections above, we are ready to prove the theorem itself. As expected for a semantic proof of soundness of a program logic, we proceed by induction on the structure of the proof π of some Hoare triple $\Gamma \vdash \{P\}C\{Q\}$. The Theorem is naturally decomposed into the two following independent statements, which we prove in turn.

THEOREM 10.3 (1-SOUNDNESS). *The comparison map $\mathcal{S} : \llbracket \pi \rrbracket_{\text{Sep}} \rightarrow \llbracket C \rrbracket_S$ is a Code 1-fibration.*

THEOREM 10.4 (2-SOUNDNESS). *The comparison map $\mathcal{L} \circ \mathcal{S} : \llbracket \pi \rrbracket_{\text{Sep}} \rightarrow \llbracket C \rrbracket_L$ is a 2-fibration.*

We detail the cases of the rules for the parallel product and the sequential product, first of 2-soundness, and then for 1-soundness, for the former is simpler.

Proof of the 2-soundness theorem. To prove that the parallel product preserves 2-fibrations, we proceed with a diagram chase in the following diagram:

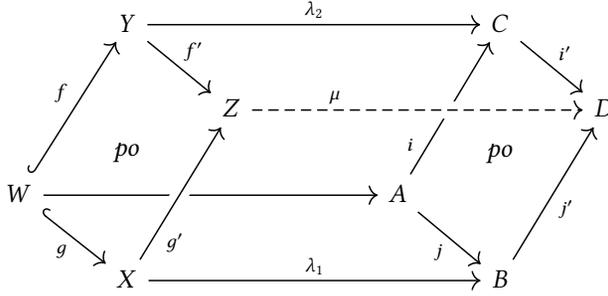
$$\begin{array}{ccc}
 \wedge & \xrightarrow{\quad} & \blacklozenge \\
 \downarrow & & \downarrow \\
 S_1 \parallel S_2 & \xrightarrow{\text{---} f_1 \parallel f_2 \text{---}} & S'_1 \parallel S'_2 \\
 \downarrow (\lambda_1, \lambda_2) & \searrow & \swarrow (\lambda'_1, \lambda'_2) \\
 & S_1 \times S_2 \xrightarrow{f_1 \times f_2} S'_1 \times S'_2 & \\
 & \downarrow & \downarrow \\
 & \mathfrak{z} \times \mathfrak{z} \xrightarrow{u \times u} \mathfrak{z}' \times \mathfrak{z}' & \\
 \downarrow (\lambda_1, \lambda_2) & \swarrow & \searrow (\lambda'_1, \lambda'_2) \\
 \mathfrak{z} \parallel & \xrightarrow{u \parallel} & \mathfrak{z}' \parallel \\
 \downarrow \text{proj} & & \downarrow \text{proj}' \\
 \mathfrak{z} & \xrightarrow{u} & \mathfrak{z}'
 \end{array}$$

where the map $f_1 \parallel f_2$ is induced by the universal property of the left pullback, and where $(u, u \parallel)$ is the span-monoidal functor structure. We also need to use that in the case of $\mathfrak{z} = \mathfrak{z}_{\text{Sep}}$, we have the following fact:

LEMMA 4. *The map $\text{pince} : \mathfrak{z}_{\text{Sep}} \parallel \rightarrow \mathfrak{z}_{\text{Sep}}$ is a 2-fibration.*

For the case of sequential composition, we heavily rely on the fact that van Kampen cubes preserve 2-fibrations using the lemma below. The preservation result then follows directly from the fact that the cube in the center of diagram (38) is van Kampen.

LEMMA 5. Consider the following diagram in **AsynGph**:



such that:

- (1) λ_1 and λ_2 are 2-fibrations,
- (2) the cube is van Kampen,
- (3) the two back faces are pullbacks.

Then, the asynchronous map μ is a 2-fibration.

PROOF. Let us assume there is a path of length 2 in Z which is mapped by μ to the top of a tile in D . We will use the fact that representables are tiny, in particular, tiles are tiny. Assume without loss of generality that the tile in D has a preimage T_C in C . Then the upper path of the tile in D is the target of a path of length 2 in Z and the target of a path of length 2 in C (the upper path of T_C). Since the cube is van Kampen, Y is the pullback of i' along μ , which means that there is a path of length 2 in Y that is mapped to the one in C . We can conclude from there using the fact that λ_2 is a 2-fibration. \square

Proof of the 1-soundness theorem. Preservation of Code 1-fibrations by sequential composition follows directly from a variant of Lemma 2 where all occurrences of Environment 1-fibrations are replaced with Code 1-fibrations. The proof that the parallel product preserves Code 1-fibrations is a bit more intricate than the case of 2-fibrations. The reason is that the map $\text{pick} : \mathfrak{z}_S^\otimes[1] \rightarrow \mathfrak{z}_S[1] \times \mathfrak{z}[1]$ is not a Code 1-fibration. Instead, we need to rely on the fact that the map $S \rightarrow \mathfrak{z}_S[1]$ from the support of the cobordism to the template is an Environment 1-fibration and on the fact that the morphism pick above never pairs two Code transitions.

LEMMA 6 (PARALLEL PRODUCT PRESERVES 1-FIBRATIONS). Given two maps of cobordisms

$$\alpha_i : C_i \rightarrow C'_i$$

for $i = 1, 2$, such that C_1 and C_2 , respectively C'_1 and C'_2 , can be sequentially composed, if α_1 and α_2 are 1-fibrations on Code transitions, then the induced morphism

$$\alpha_1 \parallel \alpha_2 : C_1 \parallel C_2 \rightarrow C'_1 \parallel C'_2$$

is a 1-fibration on Code transitions as well.

PROOF. Let us focus on the maps between the supports. We call the supports S_1, S_2, S'_1 and S'_2 , and we call the maps between that are contained in α_1 and α_2 :

$$f_1 : S_1 \longrightarrow S'_1 \quad f_2 : S_2 \longrightarrow S'_2$$

Finally, we write (λ_1, λ_2) both for the map pick in the span-monoidal structure, and for the map it induces by pullback at the level of the supports of the cobordisms.

Recall that the map $f_1 \parallel f_2$ is defined by the following diagram, where (u, u^\parallel) is the span-monoidal functor structure:

$$\begin{array}{ccc}
 \cdot & \xrightarrow{\quad} & \cdot \rightarrow_{C \cdot} \\
 \downarrow & & \downarrow \\
 S_1 \parallel S_2 & \xrightarrow{f_1 \parallel f_2} & S'_1 \parallel S'_2 \\
 \downarrow (\lambda_1, \lambda_2) & \searrow & \swarrow (\lambda'_1, \lambda'_2) \\
 & S_1 \times S_2 \xrightarrow{f_1 \times f_2} S'_1 \times S'_2 & \\
 & \downarrow & \downarrow \\
 & \mathfrak{z} \times \mathfrak{z} \xrightarrow{u \times u} \mathfrak{z}' \times \mathfrak{z}' & \\
 \downarrow (\lambda_1, \lambda_2) & \swarrow & \searrow (\lambda'_1, \lambda'_2) \\
 \mathfrak{z}^\parallel & \xrightarrow{u^\parallel} & \mathfrak{z}'^\parallel \\
 \downarrow \text{proj} & & \downarrow \text{proj}' \\
 \mathfrak{z} & \xrightarrow{u} & \mathfrak{z}'
 \end{array}$$

The image of the unique edge of $\cdot \rightarrow_{C \cdot}$ in \mathfrak{z}^\parallel is either C_1 or C_2 (recall that the polarities of \mathfrak{z}^\parallel are C_1, C_2 and F). Because the situation is symmetric, we can suppose without loss of generality that this polarity is C_1 . In that case, the edge $\cdot \rightarrow_{C \cdot}$ is mapped to a Code transition in S'_1 . By assumption, f_1 is a 1-fibration on Code transitions, so we can lift this arrow to S . Moreover, because $\lambda_1 : \mathfrak{z}^\parallel \rightarrow \mathfrak{z}$ is a 1-fibration on Code transitions as well, we can also lift uniquely $\cdot \rightarrow_{C \cdot}$ to \mathfrak{z}^\parallel . Therefore, we have the following situation:

$$\begin{array}{ccccc}
 \cdot & \xrightarrow{\quad} & S_1 \times S_2 & & \\
 \downarrow & & \swarrow \pi_1 & \langle f_1, f_2 \rangle & \searrow \pi_2 \\
 & & S_1 & \downarrow & S_2 \\
 & \xrightarrow{(1)} & \mathfrak{z}^\parallel & \downarrow \lambda_1 & \downarrow \lambda_2 \\
 & & \mathfrak{z}^\parallel[1] \times \mathfrak{z}^\parallel[1] & \downarrow \pi_1 & \downarrow \pi_2 \\
 & & \mathfrak{z}^\parallel[1] & \downarrow \lambda_1 & \downarrow \lambda_2 \\
 \cdot \rightarrow_{C_1 \cdot} & \xrightarrow{\quad} & \mathfrak{z}^\parallel[1] & \downarrow \lambda_2 & \mathfrak{z}^\parallel[1] \\
 & \searrow & & & \downarrow \lambda_2 \\
 & & \cdot \rightarrow_{F \cdot} & & \mathfrak{z}^\parallel[1]
 \end{array}$$

(2)

The map (1) exists because f_1 is a 1-fibration on Code transitions, as mentioned above, and the map (2) exists because λ_2 is a 1-fibration on Environment moves. Therefore, we can lift the arrow $\cdot \rightarrow_{C_1 \cdot}$ to $S_1 \times S_2$. We conclude by using the fact that 1-fibrations on Code transitions are stable by pushout, and that $\langle \lambda_1, \lambda_2 \rangle : \mathfrak{z}^\parallel \rightarrow \mathfrak{z} \times \mathfrak{z}$ is a 1-fibration. \square

11 RELATED WORKS

The first proof of soundness for CSL was established by Brookes [4] using a trace-based and stateless semantics. Another proof of soundness was then given a few years later by [25] and Dinsdale-Young et al. [6] using a more direct operational approach. The first proof of soundness based on a truly concurrent semantics of CSL was designed by Hayman and Winskel [11] using an encoding of the Code into Petri nets. The approach Iris [14] takes is to prove the soundness of a modal logic, and then defining Hoare triples in this logic. Our present work follows the true concurrency tradition of interpreting the parallel product as more than a simple interleaving; more specifically, we use a notion of homotopy to talk about independence of instructions, in a way closely related to directed homotopy [9, 26]. There is a well-established line of research on cospans by Bonchi et al. [2], Sassone and Sobocinski [23] for example, who generate LTSs using graph rewriting techniques based on cospans. One main difference with our work is that we use cospans to manipulate and compose our transition systems, instead of deriving them explicitly from rewriting systems.

12 CONCLUSION

One foundational and guiding principle of template game semantics is that one cannot have a direct access to the internal states of a program, because this access is necessarily mediated and regulated by the labels of a specific template \pm of interest. This idea inspired from dependent type theory implies that the basic operations on programs (composition, synchronization, errors, locks, etc.) should be defined by applying cleverly designed *change-of-basis* functors on the labeling templates. We establish in the present paper that, somewhat unexpectedly, the very same categorical yoga based on *pull* and *push* functors, works for concurrent separation logic (CSL) and for differential linear logic (DiLL). This is achieved by designing a notion of *cobordism* $\sigma : A \dashrightarrow B$ based on cospans for CSL which conveniently replaces the notion of *strategy* $\sigma : A \dashrightarrow B$ based on spans for DiLL. One nice outcome is a categorical explanation for the Hoare inequality of concurrency, which is derived here from a lax commutation property between sequential composition (understood as a colimit) and parallel product (understood as a limit). We see this healthy convergence between CSL and DiLL as a strong evidence for the relevance and surprising expressivity of template games. One main challenge for future work is to combine these two lines of research on DiLL and CSL in order to obtain an asynchronous soundness theorem for a higher-order version of CSL, based on a better understanding of the relationship with Iris [14] and FCSL [22].

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A OTHER CONSTRUCTIONS ON COBORDISMS

We present three additional constructions, having to do with union, intersection, and loops. We need the internal J -category to have a \vee -structure: a map $\vee : \mathbb{A} + \mathbb{A} \rightarrow \mathbb{A}$. Write \vee for the function on the indices, and $(l[0, ij], r[0, ij]) : \mathbb{A}[0, i] + \mathbb{A}[0, j] \rightarrow \mathbb{A}[0, i \vee j]$ for the maps of the internal functor.

A.1 Conjunction

Conjunction is defined as the pointwise pullback along the maps into $\mathbb{A}[0, i \vee j]$ that the \vee -structure gives us. The action on the interfaces is as follows; the action on the cobordisms is defined similarly by pullback. Given $\lambda : A \rightarrow \mathbb{A}[0, i]$ and $\lambda' : B \rightarrow \mathbb{A}[0, j]$, the \vee -structure gives two squares, one from λ to $id_{\mathbb{A}[0, i \vee j]}$ and another from λ' to $id_{\mathbb{A}[0, i \vee j]}$. The conjunction of λ and λ' is then the pullback of these two maps.

A.2 Disjunction

Using the same notations as for the conjunction, we define the disjunction of λ and λ' to be the obvious map $A + B \rightarrow \mathbb{A}[0, i \vee j]$.

A.3 Loops

To interpret a loop `while B do C`, we build its infinite unfolding as the least fixpoint of the map:

$$F(X) = \llbracket \text{test}(B) \rrbracket \circ \llbracket C \rrbracket \circ X \oplus \llbracket \text{test}(\neg B) \rrbracket$$

seen as an endofunctor on the category of arrows of the double category $\mathbf{Cob}(\mathbb{A})$ seen as an internal category in \mathbf{Cat} . It exists because that category has all colimits of ω -chains, and F preserves such colimits because it is itself defined using colimits.

B FILLING SYSTEMS AND THE HOARE INEQUALITY

In this section, we suppose that our template and internal opcategory \mathbb{A} is either the stateful template \mathbb{A}_S or the stateless template \mathbb{A}_L . Given two games $\lambda : A \rightarrow \mathbb{A}[0]$ and $\mu : B \rightarrow \mathbb{A}[0]$ formulated as asynchronous morphisms, let us describe $\text{fill}(\lambda, \mu)$. Every node x of A comes with a state $\text{in}_{\lambda(x)} \in \mathbb{A}[1]$ which we call the underlying state of x . Similarly, every node y of B comes with an underlying state $\text{in}_{\mu(y)} \in \mathbb{A}[1]$. The pullback $A \times_{\mathbb{A}[1]} B$ contains all the pairs $(a, b) \in A \times B$ which share the same underlying state. Hence, when we perform the pushout, we identify all such nodes a

and b ; in particular, in the case where there is another node a' of A with the same underlying states, the pullback will also contain (a', b) , and the nodes a and a' will be identified in the pushout. In summary, the support S of the filling $\text{fill}(\lambda, \mu)$ is made of three kinds of nodes:

- (1) the nodes x of A such that no node of B has the same underlying state;
- (2) the nodes y of B such that no node of A has the same underlying state;
- (3) the states $s \in \mathfrak{s}[1]$ such that there exists nodes in A and nodes in B whose underlying states is s ; we use the notation $[s]$ in order to denote these specific nodes.

Let us prove now that the filling system defined at the end of Section 5 satisfies the property that there exists a map:

$$\text{fill}(\lambda \parallel \lambda', \mu \parallel \mu') \rightarrow \text{fill}(\lambda, \mu) \parallel \text{fill}(\lambda', \mu')$$

The map is constructed in the following way. Consider a node of the support of $\text{fill}(\lambda \parallel \lambda', \mu \parallel \mu')$. As we have just mentioned, there are three possibilities:

- (1) In the first case, the element is a node of $A \parallel A'$, and thus a pair $(x, x') \in A \times A'$ consisting of two elements $x \in A$ and $x' \in A'$ with the same underlying state s . Recall indeed that the asynchronous morphism $\text{pince}[1] : \mathfrak{s}[1] \rightarrow \mathfrak{s}[1]$ is *injective on states*. Since we are in the first case, there exists no node of $B \parallel B'$ with underlying state s . This means that either:
 - (a) neither B nor B' have nodes whose underlying state is s ; in that case the node x is in $\text{fill}(\lambda, \mu)$, and the node x' is in $\text{fill}(\lambda', \mu')$, and we map (x, x') to (x, x') .
 - (b) B has a node whose underlying state is s , but not B' ; in the same way essentially as in the preceding case, we can map (x, x') to $([s], x')$,
 - (c) B' has a node whose underlying state is s , but not B ; this case is symmetric to the previous one.
- (2) the second case is symmetric to the previous one.
- (3) last case: the node of the support of $\text{fill}(\lambda \parallel \lambda', \mu \parallel \mu')$ is of the form $[s]$. In that case, there are nodes in each of the four graphs A, A', B, B' whose underlying states is s . We are thus allowed to map $[s]$ to the pair $([s], [s])$.

C SEMANTICS OF THE INSTRUCTIONS

C.1 Stateful instructions

The *machine instructions* $m \in \mathbf{Instr}$ which label the machine steps are of the following form:

$$\begin{aligned} m ::= & x := E \mid x := [E] \mid [E] := E' \mid \text{nop} \\ & \mid x := \text{alloc}(E, \ell) \mid \text{dispose}(E) \mid P(r) \mid V(r) \end{aligned}$$

where $x \in \mathbf{Var}$ is a variable, $r \in \mathbf{Locks}$ is a resource name, ℓ is a location, and E, E' are arithmetic expressions, possibly with “free” variables in \mathbf{Var} . For example, the instruction $x := E$ executed in a “machine state” $s = (\mu, L)$ assigns to the variable x the value $E(\mu) \in \mathbf{Val}$ when the value of the “expression” E can be evaluated in the memory state μ , and produces the runtime error \perp otherwise. The instruction $P(r)$ acquires the resource variable r when it is available, while the instruction $V(r)$ releases it when r is locked, as described below:

$$\begin{array}{c} \frac{E(\mu) = v}{(\mu, L) \xrightarrow{x:=E} (\mu[x \mapsto v], L)} \\ \frac{r \notin L}{(\mu, L) \xrightarrow{P(r)} (\mu, L \uplus \{r\})} \end{array} \qquad \begin{array}{c} \frac{E(\mu) \text{ not defined}}{(\mu, L) \xrightarrow{x:=E} \perp} \\ \frac{r \notin L}{(\mu, L \uplus \{r\}) \xrightarrow{V(r)} (\mu, L)} \end{array}$$

The inclusion $\mathbf{Loc} \subseteq \mathbf{Val}$ means that an expression E may also denote a location. In that case, $[E]$ refers to the value stored at location E in the heap. The instruction $x := \text{alloc}(E, \ell)$ allocates some

memory space on the heap at address $\ell \in \mathbf{Loc}$, initializes it with the value of the expression E , and assigns the address ℓ to the variable $x \in \mathbf{Var}$ if ℓ was free, otherwise there is no transition. $\text{dispose}(E)$ deallocates the location denoted by E when it is allocated, and returns \perp otherwise. Finally, the instruction nop (for no-operation) does not alter the state.

C.2 Stateless instructions

The machine instructions of the stateless semantics as given bellow, with their action on the lock state.

$$\begin{array}{ccc} L \xrightarrow{P(r)} L \uplus \{r\} & L \xrightarrow{\text{alloc}(\ell)} L & L \xrightarrow{\tau} L \\ L \uplus \{r\} \xrightarrow{V(r)} L & L \xrightarrow{\text{dispose}(\ell)} L & L \xrightarrow{m} \perp \end{array}$$

where m is a *lock instruction* of the form:

$$P(r) \mid V(r) \mid \text{alloc}(\ell) \mid \text{dispose}(\ell) \mid \tau$$

for $\ell \in \mathbf{Loc}$ and $r \in \mathbb{L}$. The purpose of these transitions is to extract from each instruction of the machine its synchronization behavior. An important special case, the transition τ represents the absence of any synchronization mechanism in an instruction like $x := E$, $x := [E]$ or $[E] := E'$.

D THE FULL PROOF SYSTEM

The syntax and the semantics of the formulas of Concurrent Separation Logic is the same as in Separation Logic. The grammar of formulas is:

$$\begin{array}{l} P, Q, R, J ::= \mathbf{emp} \mid \mathbf{true} \mid \mathbf{false} \mid P \vee Q \mid P \wedge Q \mid \neg P \\ \mid \forall v. P \mid \exists v. P \mid P * Q \mid v \overset{p}{\mapsto} w \mid \text{own}_p(x) \mid E_1 = E_2 \end{array}$$

where $x \in \mathbf{Var}$, $p \in \mathbf{Perm}$, $v, w \in \mathbf{Val}$. Given a logical state $\sigma = (s, h)$ consisting of a logical stack s and of a logical heap h , the semantics of the formulas, expressed as the predicate $\sigma \models P$, is standard:

$$\begin{array}{l} \sigma \models v \overset{p}{\mapsto} w \iff v \in \mathbf{Loc} \wedge s = \emptyset \wedge h = [v \mapsto (w, p)] \\ \sigma \models \text{own}_p(x) \iff \exists v \in \mathbf{Val}, s = [x \mapsto (v, p)] \wedge h = \emptyset \\ \sigma \models E_1 = E_2 \iff \llbracket E_1 \rrbracket = \llbracket E_2 \rrbracket \wedge \text{fv}(E_1 = E_2) \subseteq \text{vdom}(s) \\ \sigma \models P \wedge Q \iff \sigma \models P \text{ and } \sigma \models Q \\ \sigma \models P * Q \iff \exists \sigma_1 \sigma_2, \sigma = \sigma_1 * \sigma_2 \wedge \sigma_1 \models P \wedge \sigma_2 \models Q. \end{array}$$

The proof system underlying concurrent separation logic is a sequent calculus, whose sequents are Hoare triples of the form

$$\Gamma \vdash \{P\} C \{Q\}$$

where $C \in \mathbf{Code}$, P, Q are predicates, and Γ is a context, defined as a partial function with finite domain from the set \mathbf{Locks} of resource variables to predicates. Intuitively, the context $\Gamma = r_1 : J_1, \dots, r_k : J_k$ describes the invariant J_i satisfied by the resource variable r_i . The purpose of these resources is to describe the fragments of memory shared between the various threads during the execution.

$$\begin{array}{c}
 \frac{}{\Gamma \vdash \{(\text{own}_\top(x) * P) \wedge E = v\} x := E\{(\text{own}_\top(x) * P) \wedge x = v\}} \text{AFF} \\
 \\
 \frac{}{\Gamma \vdash \{E \mapsto -\}[E] := E'\{E \mapsto E'\}} \text{STORE} \\
 \\
 \frac{P \Rightarrow \text{def}(B) \quad \Gamma \vdash \{P \wedge B\} C_1 \{Q\} \quad \Gamma \vdash \{P \wedge \neg B\} C_2 \{Q\}}{\Gamma \vdash \{P\} \text{if } B \text{ then } C_1 \text{ else } C_2 \{Q\}} \text{IF} \\
 \\
 \frac{x \notin \text{fv}(E)}{\Gamma \vdash \{E \mapsto^P v * \text{own}_\top(x)\} x := [E]\{E \mapsto^P v * \text{own}_\top(x) * x = v\}} \text{LOAD} \\
 \\
 \frac{\Gamma \vdash \{P\} C_1 \{Q\} \quad \Gamma \vdash \{Q\} C_2 \{R\}}{\Gamma \vdash \{P\} C_1; C_2 \{R\}} \text{SEQ} \qquad \frac{\Gamma \vdash \{P_1\} C \{Q_1\} \quad \Gamma \vdash \{P_2\} C \{Q_2\}}{\Gamma \vdash \{P_1 \vee P_2\} C \{Q_1 \vee Q_2\}} \text{DISJ} \\
 \\
 \frac{\Gamma, r : J \vdash \{P\} C \{Q\}}{\Gamma \vdash \{P * J\} \text{resource } r \text{ do } C \{Q * J\}} \text{RES} \qquad \frac{P \Rightarrow \text{def}(B) \quad \Gamma \vdash \{(P * J) \wedge B\} C \{Q * J\}}{\Gamma, r : J \vdash \{P\} \text{with } r \text{ do } C \{Q\}} \text{WITH} \\
 \\
 \frac{\Gamma \vdash \{P_1\} C_1 \{Q_1\} \quad \Gamma \vdash \{P_2\} C_2 \{Q_2\}}{\Gamma \vdash \{P_1 * P_2\} C_1 \parallel C_2 \{Q_1 * Q_2\}} \text{PAR} \qquad \frac{\Gamma \vdash \{P\} C \{Q\}}{\Gamma \vdash \{P * R\} C \{Q * R\}} \text{FRAME}
 \end{array}$$

E PROOF OF THE SOUNDNESS THEOREM, CONTINUED

E.1 Properties of the interpretation

E.1.1 Adhesiveness and van Kampen cubes. In order to reason about the sequential composition, we use the fact that our ambient category $\mathbf{AsynGph}$ is adhesive (as is every topos, hence every presheaf category). We recall the notion from [15].

Definition E.1 (Adhesive category). A category \mathbb{S} is adhesive if (1) it has pushouts along monos, (2) it has all pullbacks, and (3) all pushouts along monomorphisms are van Kampen squares.

A **van Kampen square** is a commutative square

$$\begin{array}{ccc}
 A & \longrightarrow & B \\
 \downarrow & & \downarrow \\
 C & \longrightarrow & D
 \end{array}$$

such that for every commutative cube

$$\begin{array}{ccccc}
 Y & \longrightarrow & & \longrightarrow & Z \\
 & \searrow & & \swarrow & \uparrow \\
 & & W & \longrightarrow & X \\
 & & \downarrow & & \downarrow \\
 C & \longrightarrow & & \longrightarrow & D \\
 & \searrow & & \swarrow & \uparrow \\
 & & A & \longrightarrow & B
 \end{array}$$

whose bottom face is the square above, and whose two back faces are pullbacks, the top face is a pushout if and only if the two front faces are pullbacks. We also call such a commutative cube a **van Kampen cube**.

E.1.2 Proof of the preservation of strictness.

LEMMA 7. *The parallel product preserves strictness.*

PROOF. First, we remark that, in the definition of the parallel product, the square

$$\begin{array}{ccc} I_1 \parallel I_2 & \longrightarrow & C_1 \parallel C_2 \\ \downarrow & & \downarrow \\ I_1 \times I_2 & \longrightarrow & C_1 \times C_2 \end{array}$$

is a pullback square. The reason is that in the following cube, the bottom, right and left faces are pullback squares:

$$\begin{array}{ccccc} I_1 \times I_2 & \longrightarrow & C_1 \times C_2 & & \\ \downarrow & \swarrow & \downarrow & \swarrow & \\ \mathfrak{z}[0] \times \mathfrak{z}[0] & \longrightarrow & \mathfrak{z}[1] \times \mathfrak{z}[1] & & \\ \downarrow & \swarrow & \downarrow & \swarrow & \\ \mathfrak{z}^{\parallel}[0] & \longrightarrow & \mathfrak{z}^{\parallel}[1] & & \end{array}$$

The lateral faces are defined to be pullbacks, and the bottom one is a pullback for the three templates that we use. Now, we consider the following cube:

$$\begin{array}{ccccc} I_1 \times I_2 & \longrightarrow & C_1 \times C_2 & & \\ \downarrow & \swarrow & \downarrow & \swarrow & \\ I_1' \times I_2' & \longrightarrow & C_1' \times C_2' & & \\ \downarrow & \swarrow & \downarrow & \swarrow & \\ I_1' \parallel I_2' & \longrightarrow & C_1' \parallel C_2' & & \end{array}$$

According to the remark above, the top and bottom faces are pullbacks, and by hypothesis the back face is a pullback. This concludes the proof. \square

E.2 Fibrational properties of change of lock operations

Recall that change of locks are a push-then-pull operation, where pulling is achieved by a pullback and pushing by postcomposition. As fibrations, epis and monos are preserved under pullbacks in our ambient category, pulling preserves all the structural properties of our cobordisms. Hence, the preservation of the properties of the cobordisms under change of locks will be determined by the properties of the asynchronous we push along.

LEMMA 8. *Given an acute span of J -opcategories*

$$\mathfrak{z}_1 \xleftarrow{F} \mathfrak{z}_2 \xrightarrow{G} \mathfrak{z}_3$$

and a cobordism C in $\mathbf{Cob}(\mathfrak{z}_1)$ which satisfies the structural properties of section 10.3:

- if every map $g[i] : \mathfrak{s}_2[i] \rightarrow \mathfrak{s}_3[f(i)]$ is an epi, then condition (1) is preserved,
- if every map $g[ij] : \mathfrak{s}_2[ij] \rightarrow \mathfrak{s}_3[f(ij)]$ is an Environment 1-fibration, then condition (2) is preserved.

In the case of critical sections, we only use the third condition above, as the other condition is recovered afterward through the sequential compositions with $\llbracket P(r) \rrbracket$ and $\llbracket V(r) \rrbracket$ which determine the input and the output states of $\llbracket \text{with } r \text{ do } C \rrbracket$. In the case of the semantics of proofs, this corresponds to the fact that initial states of a critical section must be states where the lock is held by the code. The case for hiding is more straightforward as the components of hide_C are epimorphisms and Environment 1-fibrations. Thus, we have established that change of locks preserve the structural properties of cobordisms. We now prove that they also preserve the structural properties of maps between cobordisms, using the following lemma:

LEMMA 9. Given a pair of plain internal functors of J -opcategories

$$F : \mathfrak{s}_2 \rightarrow \mathfrak{s}_1 \quad \text{and} \quad F' : \mathfrak{s}'_2 \rightarrow \mathfrak{s}'_1$$

and two internal functors of J -opcategories

$$G_1 : \mathfrak{s}_1 \rightarrow \mathfrak{s}'_1 \quad \text{and} \quad G_2 : \mathfrak{s}_2 \rightarrow \mathfrak{s}'_2$$

such that the obvious square that they compose commutes, and given two cobordisms C and C' in $\text{Cob}(\mathfrak{s}_1)$ and $\text{Cob}(\mathfrak{s}_2)$ respectively, together with a comparison map \mathcal{L} between them that sits over G_1 , then operation of pulling along F and F' gives rise to two cobordisms in $\text{Cob}(\mathfrak{s}'_1)$ and $\text{Cob}(\mathfrak{s}'_2)$ respectively, together with a comparison map \mathcal{L}' which sits over the internal functor G_2 . Assuming \mathcal{L} is strict, the induced map \mathcal{L}' is strict as well providing that the following squares are pullbacks:

$$\begin{array}{ccc} \mathfrak{s}_2[i] & \longrightarrow & \mathfrak{s}_2[ij] \\ F \downarrow & & \downarrow F \\ \mathfrak{s}_1[i] & \longrightarrow & \mathfrak{s}_1[ij] \end{array} \quad \begin{array}{ccc} \mathfrak{s}'_2[i] & \longrightarrow & \mathfrak{s}'_2[ij] \\ F' \downarrow & & \downarrow F' \\ \mathfrak{s}'_1[i] & \longrightarrow & \mathfrak{s}'_1[ij]. \end{array}$$

PROOF. We use the pasting lemma for pullbacks in the cube on the left and its counterpart with primes, and then again in the right cube.

$$\begin{array}{ccccc} I & \xrightarrow{\quad} & C & & \\ \downarrow & \swarrow & \downarrow & \swarrow & \\ \mathfrak{s}_1[i] & \xrightarrow{\quad} & \mathfrak{s}_1[ij] & & \\ \downarrow & \swarrow & \downarrow & \swarrow & \\ \mathfrak{s}_2[i] & \xrightarrow{\quad} & \mathfrak{s}_2[ij] & & \\ \downarrow & \swarrow & \downarrow & \swarrow & \\ I_1 & \xrightarrow{\quad} & C_1 & & \\ \downarrow & \swarrow & \downarrow & \swarrow & \\ \mathfrak{s}'_1[i] & \xrightarrow{\quad} & \mathfrak{s}'_1[ij] & & \\ \downarrow & \swarrow & \downarrow & \swarrow & \\ I'_1 & \xrightarrow{\quad} & C'_1 & & \end{array}$$

□

The last fibrational property we would like the change of lock construction to preserve is the fact that the comparison morphism itself being a fibration of some kind. The following lemma will handle each of our uses of the construction.

LEMMA 10. Using the same assumptions and notations as the the preceding lemma, if the components

$$F[ij] : \mathfrak{s}_2[ij] \rightarrow \mathfrak{s}_1[ij]$$

are fibrations defined by a right lifting property wrt $S_1 \rightarrow S_2$ and if, for every pairs of maps l and r , if the following diagram commutes:

$$S_2 \begin{array}{c} \xrightarrow{l} \\ \xrightarrow{r} \end{array} \triangleright \mathfrak{z}'_1[1] \longrightarrow \mathfrak{z}'_2[1]$$

then $l = r$, then the change of locks construction preserves that kind of fibration.

PROOF. The crux of the proof is the rely on the fact that given that the following square is a pullback

$$\begin{array}{ccc} C & \longleftarrow & C_1 \\ \downarrow & & \downarrow \\ \mathfrak{z}_1[1] & \longleftarrow & \mathfrak{z}_2[1] \end{array}$$

the map $C_1 \rightarrow C$ is a fibration as well using the hypothesis on F . Then, one concludes by a diagram chasing and the second hypothesis. \square

E.3 Other constructions

It is easy to check that the *coproduct of cobordisms* preserves all the properties of the cobordisms and of comparison maps between that we consider. From this and from the properties established about sequential composition, we deduce that it is also the case for *conditionals*, as they are defined as a combination of these constructions.

For the case of loops, this follows from the fact that loops are defined as an unfolding of sequential compositions and of sums.